KENWOOD

SFRVICE MANUA

TM-2550A/E,TM-2530A MU-1, MB-10,TU-7, PG-2K,VS-1

144MHz FM TRANSCEIVER



Photo is TM-2550A.

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CIRCUIT DESECRIPTION

	TM-2550A (K1,M1,M3)	TM-2550E (T,W)	TM-2530A (K2,M2,M4)
FINAL UNIT	X45-1440-10	X45-1440-10	X45-1440-11
PLL UNIT	X50-2030-10	X50-2030-51	X50-2030-11
	X53-1440-10 (K1,M1)	X53-1440-51 (T)	X53-1440-10 (K2,M2)
CONTROL UNIT	X53-1440-21 (M3)	X53-1440-61 (W)	X53-1440-21 (M4)
DISPLAY UNIT	X54-1860-10	X54-1860-10	X54-1860-10
COMPOUND UNIT	X60-1280-10	X60-1280-51	X60-1280-10

Table 1 TM-2550A/E, TM-2530A PC board chart

RX SECTION

Signals from the antenna are applied to the FINAL unit (X60-1280-XX) transmit receive swithing circuit, D2 and D4. In receive, diodes D2 and D4 are reverse biased forcing the incoming receive signal down thru L3 to the RA (Receive Antenna) terminal. There the incoming receive signal is then coupled to the COMPOUND Unit (X60-1280-XX). The signal is filtered by a low pass filter and amplified by Q1 the first RF amplifier, a GaAs FET: 3SK129(S). The amplified RF signal is then mixed with the LR (Receiver Local Oscillator) signal in the first mixer Q2: 3SK74(L) to obtain the First IF frequency of 10.695MHz. This IF signal is filtered by Helical Resonator, L4, and a two stage MCF (Monolithic Crystal Filter), L8, which combine to provide excellent two-signal characteristics and sensitivity.

The First IF signal from the MCF is then amplified by the First IF amplifier Q3: 2SC2668(Y) and applied to the FM IF Amplifier/Mixer/Detector, IC2: TA7761P. This signal IC mixes the IF signal with the 10.24MHz 2nd Local Oscillator signal (L14) to obtain the 2nd IF frequency of 455kHz, then amplifies, routes the signal thru external filter L16, and finally convertes it into the received audio signal with the internal quadrature detector. The 2nd Local Oscillator signal from L14 and IC2 is also used by the PLL unit as a Reference signal.

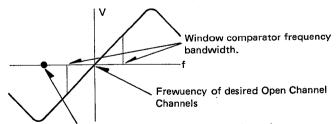
The detected audio output from IC2 is applied to the SQUELCH CONTROL (X59-1040-10), and to the deemphasis circuit R56 and C99. The (de-emphasized) audio is amplified by Audio preamplifier Q9 : 2SC2458(Y) and then applied thru the AF GAIN control to the Audio Amplifier IC1 : μ PC1241H which drives the speaker.

The noise component of the detected audio is filtered by a band-pass filter, in order to obtain a frequency of approximately 30kHz (which is well outside the normal voice bandwidth) and then amplified by the noise amplifiers contained inside IC2. This amplified noise is rectified by diodes D6 and D5 and applied to Q1 of the Squelch Switch. Q1 is used to control the conduction of Q2, which controls AF Preamp Q9.

A center-stop tuning circuit is provided to ensure that the incoming signal is actually on frequency before the squelch is opened. A portion of the 455kHz IF signal is coupled thru the secondary of L17 and applied to CENTER STOP unit (X59-1030-10) where Ceramic Discriminator L12, and Window comparator IC1: NJM4558M are used to determine if the incoming signal is actually the channel center. When the detected signal applied to the window comparator reaches a predetermined level, Q5 of the Squelch Switch will be turned OFF or ON. The Squelch Switch determines if there is an incoming signal at the same time. If the incoming signal is at the proper level, and on frequency, the squeich will open and the C.TUNE indicator in the LCD display will turn ON. This indicator is controlled by IC2, the main microprocessor on the CONTROL unit (X53-1440-XX), thru inputs supplied from the COM-POUND unit via the BD line. During Open Channel Scanning of the DCL system Q3, Q1, and Q2 of the Squelch Switch are controlled by the main microprocessor via the SQS line. During DCL operations open channel search threshold levels are controlled by VR3 of the Compound ass'y. Transistor Q6 of the Squelch Switch, is used to prevent the Center Stop circuit from activating during Open Channel Scan Operation.

Note 1:

The Center Stop circuit is not activated during Open Channel Scan.



Interference such as IMDfrom strong adjacent channels makes the use of the Center Stop function erratic during Open channel scan.

Fig. 1

Audio Preamplifier Q9 is controlled by Q1 of the Alert, Vacant CH. unit (X59-1020-10) during Alert, code Squelch and Open Channel operations.

CIRCUIT DESECRIPTION

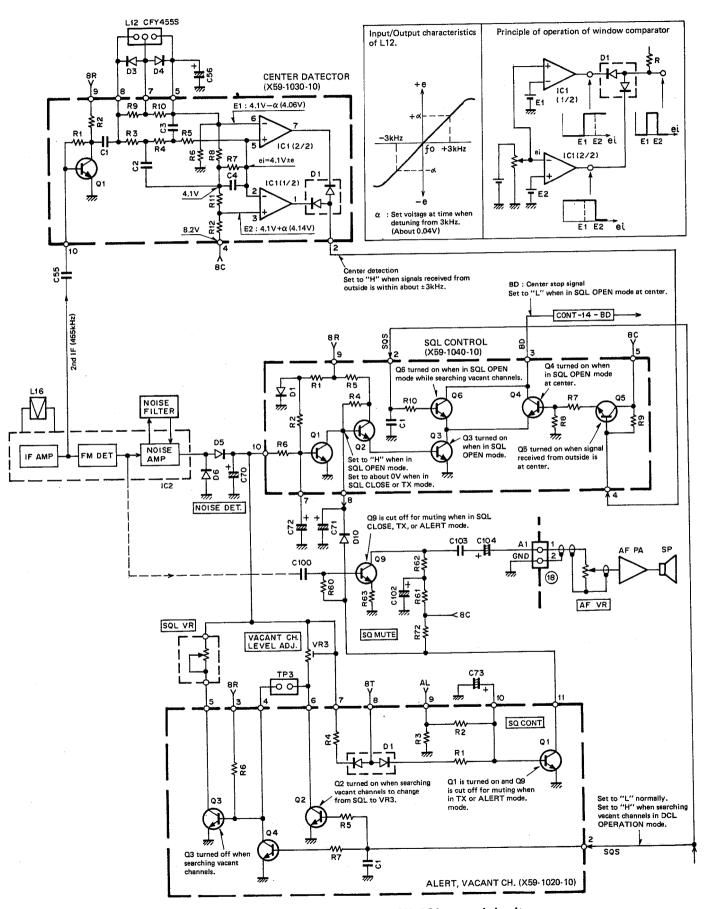


Fig. 2 Center datector, Alert, Vacant CH, SQL control circuit

CIRCUIT DESECRIPTION

l tem	Rating				
Nominal center frequency (fo)	10.695MHz				
Pass bandwidth	±7.5kHz or more at 3dB				
Attenuation bandwidth	±25kHz or less at 40dB ±45kHz or less at 60dB				
Guraranteed attenuation	 70dB or more within ±1MHz Spurious level = 40dB or more at fo—fo + 500kHz Spurious level = 80dB or more at fo—(910kHz±10kHz) 				
Ripple	1.0dB or less				
Loss	1.5dB or less				
Impedance	3k Ω /0pF				

ltem	Rating				
Nominal center frequency (fo)	455kHz				
6dB bandwidth	±6kHz or more				
50dB bandwidth	±12.5kHz or more				
Ripple (within 455±5kHz)	3dB or less				
Loss	6dB or less				
Guaranteed attenuation (within 455±100kHz)	35dB or more				
Input and output impedance	2.0kΩ				

Table 3 Ceramic filter (L72-0315-05) COMP unit L16

Table 2 MCF (L71-0216-05) COMP unit L8

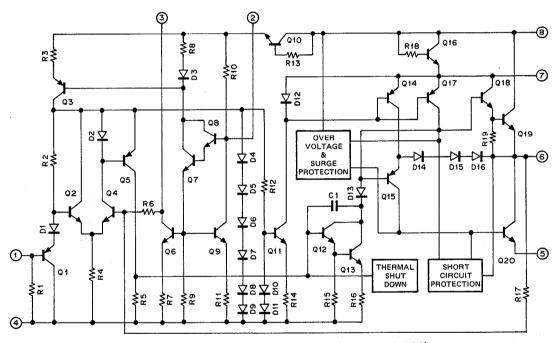


Fig. 3 μ PC1241H Equivalent circuit (COMP unit IC1)

TX SECTION

Incoming microphone audio from the DISPLAY unit (X54-1860-10 C/3) is amplified by Q8: 2SC1775(E). This amplified audio is then buffered by buffer amplifier Q7: 2SC2458(Y) and applied to D2: 1SS181 of the MIC AMP/S METER unit (X59-1010-10) for limiting. The signal is then amplified by a portion of IC1: NJM4558M on the MIC AMP/S METER unit. D2 of the MIC AMP unit (X59-1000-10) provides additional limiting. The microphone audio is filtered by a 2 stage active LPF, IC1: NJM4558M and applied to varactor diode D3: 1S2208 in the PLL unit (X50-2030-XX) for phase modulation of the transmit VCO, Q8.

The phase-modulated FM signal is amplified by driver transistors Q10: 2SC2688(Y), Q11: 2SC2347 and Q12: 2SC2538-22-A (TM-2530) or 2SC3019 (TM-2550) and applied to the FINAL unit (X45-1440-10: TM-2550A/E or X45-1440-11: TM-2530A).

This signal is amplified by the Hybrid Final Amplifier Module Q1: M57737 (TM-2530A) or M57726 (TM-2550A/E) and applied to the antenna via the transmit/receive switching diode D2. During transmit, forward bias is applied to D2 allowing the transmit signal to pass. Diode D3 is also forward biased during transmit. The value of L3 was selected to present a high impedance to the transmitted signal. Any of the transmit signal that passes thru L3 is shunted to ground thru D3 to protect the receiver circuits and prevent feedback. Once the signal has been passed thru D2 it proceedes thru the LPF and to the antenna.

The APC (Automatic Protection Circuit) on the FINAL unit consists of two circuit, a negative feedback circuit that senses output power and an SWR protection circuit.

CIRCUIT DESECRIPTION

The negative feedback circuit samples the transmitted signal, rectifies this signal with D4, and applies the rectified signal to NF GAIN amplifier Q6: 2SC2458(Y). This amplifier supplies a signal to Differential Amplifiers Q4 and Q5: 2SC2458(Y) which control the bias applied to the PA module pin 2, and driver transistor Q12 of the PLL unit, via bias regulators Q2: 2SD1406(Y) and Q3: 2SA1015(Y). High/Low power swithing is accomplished by applying a ground to pin number 1 of connector (2) on the FINAL unit. This causes VR3 to act as a voltage divider that controls the condution of NF GAIN amplifier Q6.

Reflected power is coupled thru D5 on the FINAL unit and is used to control the conduction of Q7. This transistor function like Q6 to control differential amplifers Q4 and Q5, and thus the bias applied to the Power Amplifier.

Item	Symbol	Tc(°C)	Condition	Rating
Operating	Vcc	25		17V
DC current	Icc	25		7A
Operating case temp.	Tc (op)		-30~ + 110°C	
Storage temp.	Tstg		-40~ + 110°C	
Power input	Pin	25	$Zg = ZI = 50\Omega$	0.4W
Power output	Po	25	$Zg = ZI = 50\Omega$	40W

Table 4 M57737 Max. rating (TM-2530A Final unit Q1)

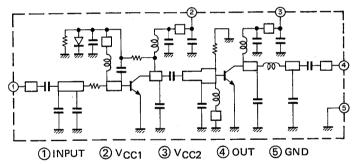


Fig. 4 M57737 Equivalent circuit

Item	Symbol	Tc (°C)	Rating
Operating voltage	Vcc	25	17V
DC current	Icc	25	14A
Operating case temp.	Tc (op)		-30~ + 110°C
Storage temp.	Tstg		_40~ + 110°C

Table 5 M57726 Max. rating (TM-2550A/E Final unit Q1)

Item	Symbol	Tc (°C)	Condition	Value		
i tem	O, m.bo.	101 07		Min.	Тур.	
Power output	Po	Po 25 $Vcc = 12.5V$. $F = 144 \sim 148 MHz$, $P_1N = 0.4W$, $Z_L = Z_G = 50\Omega$		43W	47W	
Total efficiency	ηΤ	. 25	Vcc = 12.5V, F = 144~148MHz, PIN = 0.4W, ZL = ZG = 50Ω	50%	54%	

Table 6 M57726 Electrical characteristic

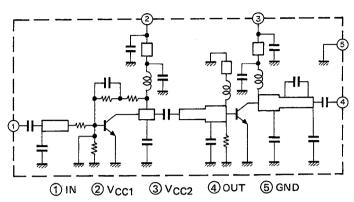


Fig. 5 M57726 Equivalent circuit

PLL CIRCUIT

The PLL circuit is divided into two main loops: transmit and receive.

Receive PLL Loop

The signal generated by the RX VCO (Voltage Controlled Oscillator) Q16: 2SK192A(GR)*P is applied to buffer amplifier Q17: 2SC2668(Y) and mixed with the HET (Heterodyne) signal by Q21: 2SC2668(Y) where it becomes the PLL IF signal (**K,M** 13.015 to 15.010MHz, **T,W** 12.865 to 19.86MHz). This PLL IF signal is then amplified by Q20: 2SC2668(Y) and applied to the Phase Detector IC2: MC145155P*K where it is divided to obtain a 5kHz signal. The divide ratio is determined by Serial Data from the CONTROL unit. This 5kHz signal is compared with the 5kHz reference signal obtained by dividing the 10.240 MHz Reference signal.

The Phase Detector compares the phase of these two signals and transmits an error control signal to the VCO. The control signal is filtered by an Active Low Pass filter composed of Q13: 2SK30A(O) and Q14: 2SC2458(Y) to remove any AC fluctuations to obtain a DC correction voltage. The correction voltage is used to change the capacitance of Varactor Diode D5: 1SV50, which varies the output of the RX VCO to lock it on frequency.

If the phase difference is too great to be corrected by the control voltage applied to D5, an unlock signal is generated by the Phase Detector. This signal turns OFF Q15: 2SC2458(Y) which turns OFF output amplifier Q18: 2SC2668(Y) to prevent operation outside the authorized limits.

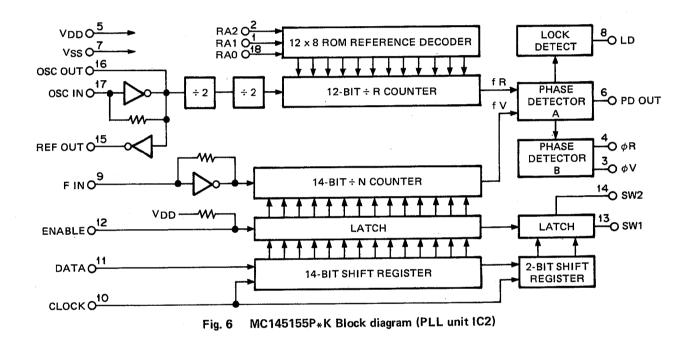
The PLL HET Oscillator, Q19: 2SC2668(Y) oscillates at 39.48MHz. This signal is applied to frequency tripler Q22: 2SC2688(Y) to obtain a signal of 118.44MHz which is applied to mixer Q21.

CIRCUIT DESECRIPTION

Transmit PLL Loop

The signal generated by the TX VCO Q8: 2SK125 (**K,M** 142.00 to 148.995MHz, **T,W** 144.00 to 145.995MHz) is mixed with the RX PLL output, by Q7: 2SC2688(Y) and applied to the Transmit Phase Detector IC1: MC145151P. This signal and the 10.240MHz signal from IC2 are divided by 512, to obtain 20kHz. These two signals are compared and an error correction signal is applied to the Active Low Pass filter Q3, Q4 and Q5: 2SC1775(E). Comparision at a frequency of 20kHz provides shorter response times, a real necessity in transmit. The DC correction voltage from the LPF is used to control varactor diode D2: 1SV50.

The divide ratio is increased by 91 during transmit operations. This causes the TX VCO frequency to be locked at a frequency 455kHz higher than the RX VCO frequency which prevents internal mixing. When an error occurs that is greater than the correction voltage limits an unlock signal is generated by IC1. This signal controls the conduction of Q1: 2SC2458(Y) which controls Q2: 2SA1048(Y) to stop transmissions. Q2 interrupts the bias of the TX driver stage.



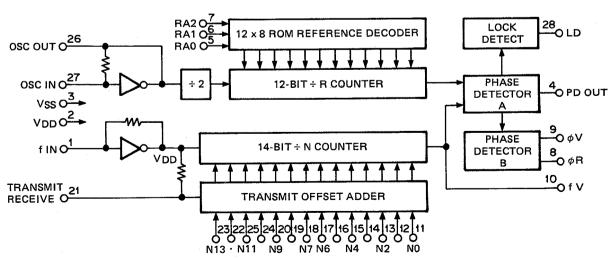


Fig. 7 MC145151P Block diagram (PLL unit IC1)

CIRCUIT DESECRIPTION

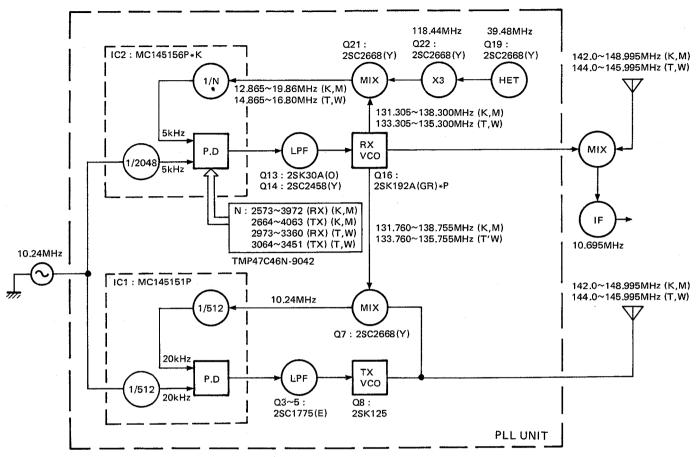
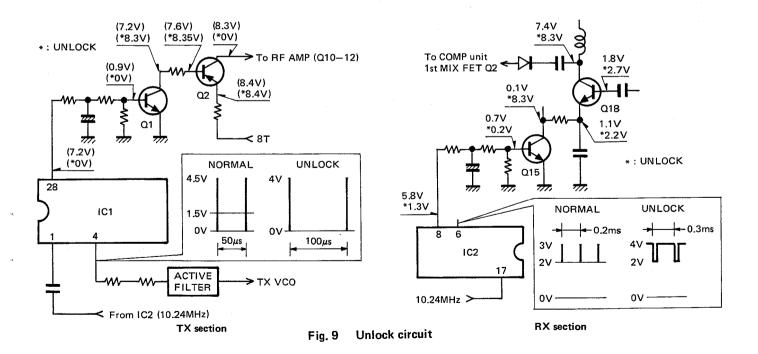


Fig. 8 Frequency-related block diagram



CIRCUIT DESECRIPTION

CONTROL UNIT (X53-1440-XX)

The CONTROL unit uses two Microprocessors. the Main Microprocessor IC2: TMP47C46N-9042 and Auxilliary Microprocessor IC3: μ PD75008HC-056. Both utilize 4 bit architecture and have 4K Bytes of ROM available.

The Main Microprocessor controls; frequency, offset, tone, memory frequency, key, switch, encoder, frequency display, DCL system and the Auxilliary Microprocessor. The Auxilliary Microprocessor controls; the voice synthesizer, display/contrl/and memory for telephone number functions, tone frequencies (K,M), audio oscillator, and digital signal conversion and analysis processing for DCL system.

The Main Microprocessor utilizes a serial interface bus to the Auxilliary processor. A CMOS static RAM, IC6: TC5047AP-1 with a capacity of 1K x 4 bits, is used for external memory storage of operator programmed data such as memory frequencies and telephone number data. It also functions as a data buffer when exchanging data between the Main and Auxilliary processors. Memory addressing is performed by IC5: TC40H374P with read and write operations being handled by the Data Bus.

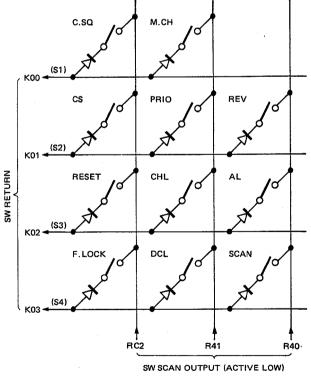


Fig. 10 Diode matrix

Switch Section

With a few exceptions most switch inputs on the front panel are arranged in a diode matrix. The Main Microprocessor reads the inputs via a keyboard status scan system. This system determines which key, if any, has been depressed.

The diode matrix is illustrated in Fig. 10.

The PHONE switch **(K,M)**, P.MR switch **(T,W)** TONE switch, PTT switch, UP switch and DOWN switch all supply inputs directly to the Main Microprocessor.

Keyboard section

Fig. 11 illustrates the keyboard section circuit diagram. The keyboard receives scan pulses from the Main Microprocessor ports P10 thru P13. The current keyboard status is returned via ports R70 thru R73. Pull-up resistors of P10 thru P13 are controlled by port R90. Keyboard status is scanned when R90 is pulled up from logic low by Q3: DTA114YF.

Display section

Fig. 11 shows the layout of the LCD.

The LCD driver (Keyboard ass'y) is controlled via 8 data lines and 4 control lines on a parallel bus system by the Main and Auxilliary microprocessors.

Data transfer is possible only when the CS line is Low and in conjuction with the leading edge of the WE pulse. The DCL and CQS LED's are lit by transistors Q4 and Q5 which are controlled by the Main Microprocessor.

CIRCUIT DESECRIPTION

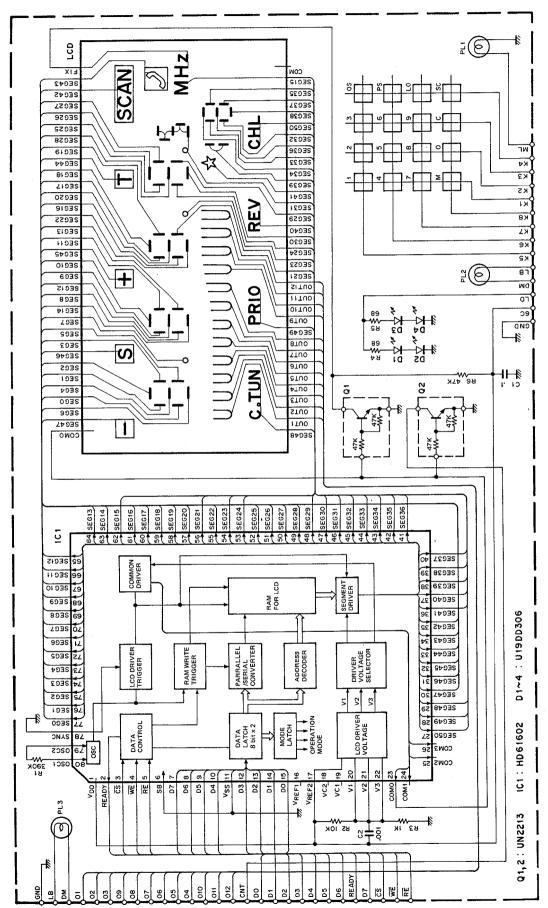


Fig. 11 Keyboard section circuit diagram

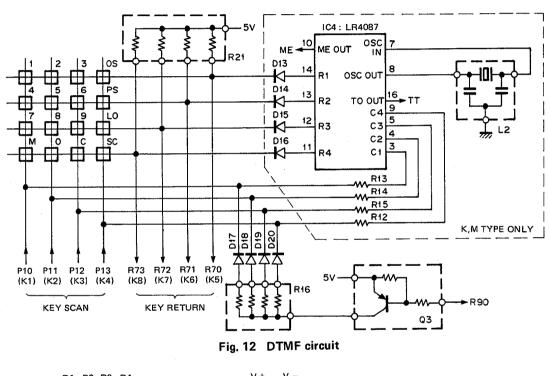
CIRCUIT DESECRIPTION

DTMF circuit (K,M Type)

The DTMF (Dual Tone Multiple Frequency) signal is generated by IC4: LR4087 on the CONTROL unit. Power for the tone generator is obtained from the 8T (8 volts on transmit) line. During receive the IC is grounded preventing unwanted tone generation.

During transmit the non-exclusive port R90 is a logic High, causing pull-up resistors P10 thru P13 to open allowing the DTMF signals to be generated by pressing one of the keyboard keys.

During automatic transmission of telephone number information port R90 is a logic Low, connecting pull-up resistors P10 thru P13. P10 thru P13 apply a logic High, and P70 thru P73 a logic low, so that the desired DTMF signals are generated. While the DTMF signal is being generated IC4 (CONTROL unit) mutes the normal transmitted audio.



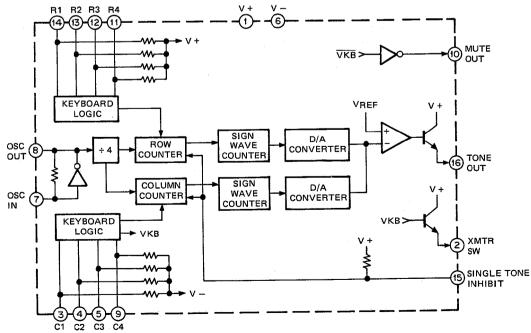


Fig.13 LR4087 Block diagram (Control unit IC4) K, M type only

CIRCUIT DESECRIPTION

PLL and Tone data (K,M type)

PLL and Tone data are sent from the Main Microprocessor on P30 and the Clock signal is on P33. Data and Clock signals are used concurrently. The Enable signal is transmitted on P21 for PLL data and on RC3 for tone data.

Audio Oscillator (CONTROL unit)

An audible confirmation of various keyboard inputs is provided by the TM-2530/50. This audio output is controlled via line P21 of the Auxilliary Microprocessor, IC3. The Oscillator is a portion of IC1: MC14584BCP and its output is supplied to the COMPOUND unit via the BZ line.

Voice Synthesizer Control Section

The Main Microprocessor (IC2) analyzes inputs from the VOICE unit, and provides control information to the Auxilliary Microprocessor (IC3) necessary for processing the Voice data.

The Auxilliary Microprocessor provides control input and output via data lines PSO thru PS4, SR and BY.

Rotary Encoder Section

Fig. 14 shows the encoder output waveforms. Waveform B is used as a reference. The phase of waveform A is compared with that of waveform B to determine if the dial base been turned clockwise or counterclockwise. A portion of IC1 is used as a Schmitt trigger to filter out any chattering of the encoder contacts. The inverted waveforms from IC1 are applied to the Main Microprocessor on pins RP0 and RP1, where the phase of the waveforms is compared, the number of input pulses is also counted by the microprocessor to detemine how much of a frequency change has been ordered.

Other Input and Output Sections

1) Standby or push-to-talk

PTT switch status is sent to the Main Microprocessor via the SS terminal on PS1. During manual or automatic transmission a logic Low is supplied on the RA2 line to the COMPOUND unit. This signal turns on the transmit voltage regulator and turns OFF the receive voltage regulator.

2) Microphone UP/DOWN switches

Any switch chattering (Keyboard) is filtered by C11 and C12 and the inputs from the UP and DOWN switch are applied to the Main Microprocessor on pins RB3 and RB2 for processing.

3) Busy indicator (BD)

A signal is supplied from the COMPOUND unit Center Tune circuit to the Main Microprocessor via pin RB2 to indicate when the radio has received an input and squelch has opened.

4) Microphone Muting

In order to prevent inteference to the Digital signals transmitted when using the DCL system the microphone must be muted. This function is controlled by Auxilliary Microprocessor via pin P61.

5) Squelch Switch (SC)

The Main Microprocessor provides a signal to control the Squelch threshold level during Digital Channel Linkage operations. Specifically this occurs when the radio is scanning for an open channel. When the SC terminal is a logic low, VR3 of the COMPOUND unit is used to control the Squelch Threshold.

6) RX Audio Muting (AL)

The Main Micorprocessor provides a logic High on P32 to mute the receive audio during Code Squelch operation, Open Channel search operations, and Priority channel scanning.

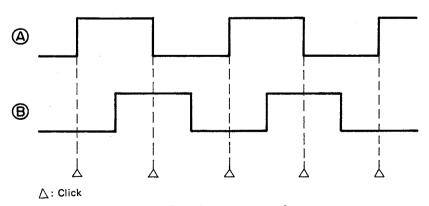


Fig. 14 Encoder output waveforms

CIRCUIT DESECRIPTION

Resetting the Microprocessor

To reset the Main Microprocessor (return to the default settings) ground pin number 49 of the Main Microprocessor. This should normally only be required when replacing the Lithium battery.

The Auxilliary microprocessor is reset by a reset pulse from IC7: PST520D.

NOTE 2: The operating system of this radio is in NON-ERASEABLE memory in the microprocessors. Replacing/removing the battery will not require reprogramming of Operating System paramaters. Operator programmed information (telephone numbers, memory frequencies, etc.) will require reentry.

Memory backup

Transistor Q2: 2SC2458(Y) and zener diode D3: MTZ7.5JA sense when the voltage supplied to the DC power connector drops below approximately 7.5 Vdc from its normal 13.8 Vdc. If the supply voltage falls below this minimum a control signal is supplied to the HOLD terminal (pin 52) of the Main Microprocessor via a Schmitt trigger. The microprocessor then enters the backup mode and draws power from the Lithium battery.

DCL Contro System

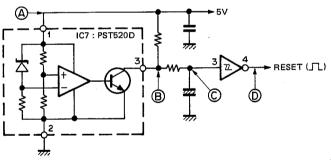
A block diagram of the DCL control system is shown in Fig. 16

Control of Open Channel searching is performed by the Main Microprocessor. The microprocessor searches for an open channel by controlling the frequency of the receiver section. When an open channel is found the frequency data of that channel is stored into RAM and a control signal is applied to the Auxilliary microprocessor. The Auxilliary microprocessor takes this signal, reads the frequency data that was stored in the RAM, generates the Digital Control signal, and applies this data to the MODEM unit (X57-1140-20). The MODEM unit uses this incoming data to generate the MSK (Minimum Shift Keying) signal which is applied to the COMPOUND unit for transmission.

A portion of the incoming receive signal is applied to the RD terminal of the MODEM unit. The MODEM unit detects the prescence of any Digital Control signal and if present relays it to the Auxilliary Microprocessor.

The Auxilliary microprocessor compares the incoming DCL signal with the preprogrammed DCL data maintained in memory. If the signals are equal a request is made to transfer the incoming data into RAM. The Main microprocessor detects this transfer request and passes the data into RAM, and transmits a signal signifying data has been received, is sounds an audible alarm.

The Main microprocessor performs any frequency shifts or code squelch functions, determined by the incoming data.



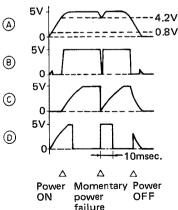


Fig. 15 Reset circuit and waveforms at respective points

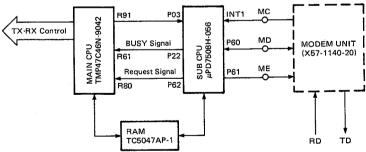


Fig. 16 DCL control system block diagram

CIRCUIT DESECRIPTION

Pin No.	Name	In/Out	Function	Logic	Pin No.	Name	In/Out	Function	Logic
11	RD0	1	Encoder E1		33	P20	0	1750Hz Tone Control (T,W)	
2	RD1	1	Encoder E2		34	P21	0	PLL Enable Signal	
3	R40	1/0	Data Bus (D0)		35	P22	0	HD61602 RE Signal	15-7
4	R41	1/0	Data Bus (D1)		36	P23	0	HD61602 WR Signal	7 -
5	R42	1/0	Data Bus (D2)		37	P30	0	PLL (Tone) Data Output	
6	R43	1/0	Data Bus (D3)		38	P31	0	Squelch Control Signal	
7	R50	1/0	Data Bus (D4)		39	P32	0	Audio Mute Signal	
8	R51	1/0	Data Bus (D5)		40	P33	0	PLL (Tone) Clock Output	
9	R52	1/0	Data Bus (D6)		41	RB0	1	VOICE SW Input	17.
10	R53	1/0	Data Bus (D7)		42	RB1	1	PHONE (K,M) SW input	
11	R60	1	HD61602 READY Signal					P.MR (T.W) SW input	
12	R61	ı	SUB CPU Busy Signal		43	RB2	1	MIC Down SW Input	17 6
13	R62	-	Busy Signal		44	RB3	1	MIC Up SW Input	1
14	R63	ı	Tone SW Input	77 [45	K00	1	SW Return (S1)	
15	R70	1	16 Key Return (K1)		46	K01	i	SW Return (S2)	
16	R71	1	16 Key Return (K2)		47	K02	ı	SW Return (S3)	
17	R72	I	16 Key Return (K3)		48	K03	ı	SW Return (S4)	
18	R73	I	16 Key Return (K4)		49	RESET		Reset Input	7 6
19	RA0	0	Memory Lamp		50	Xin		Clock	
20	RA1	0	RAM OD Signal Output		51	Xout		Clock	
21	RA2	0	Standby Signal Output		52	HOLD		Back up Information Input	ו ר
22	RA3	0	RAM A8 Signal Output		53	R80	1	SUB CPU Request Signal	
23	P00	0	Address Latch		54	R81	1	PTT SW Input	
24	P01	0	RAM CE1 Signal Output		55	R82	1	DCL Diode Matrix Input	
25	P02	0	HD61602 CS Signal Output		56	R83 .	ì	SCAN Timer Trigger pulse	
26	P03	0	RAM CE2 Signal Output		57	R90	0	Keyboard select	
27	P10	0	16 Key Scan (K5)		58	R91	0	Serial Data Output	
28	P11	0	16 Key Scan (K6)		59	R92	0	Serial Clock Output	
29	P12	0	16 Key Scan (K7)		60	RC0	0	LED (DCL) Control	17
30	P13	0	16 Key Scan (K8)		61	RC1	0	LED (C.SQ) Control	
31	TEST		GND		62	RC2	0	SW Scan	
32	Vss		GND		63	RC3	0	Tone DATA LOAD Signal	
\		1			64	VDD		Power Supply	1 -

Table 7 TMP47C46N-9042 Terminal functions (Control unit IC2)

Pin No.	Name	In/Out	Function	Logic	Pin No.	Name	In/Out	Function	Logic
1	00UT		Open		21	CL2		Clock	
2	P20	0	VS-1 PS4		22	INT1	J	Modem Clock Input	
3	P21	0	"Beeper" Switching		23	P00	ı	Backup Clock Input	
4	P22	0	MAIN CPU Busy Signal		24	P01	1	Serial Clock Input	
5	P23	0	VS-1 SR		25	P02		Open	
6	P10	0	Adress Latch		26	P03	1	Serial Data Input	
7	P11				27	P60	1/0	Modem Data Input/Output	
8	P12	1.	HD61602 READY Signal		28	P61	0	Modem Me Signal Output	
9	P13	0	VS-1 BY		29	P62	0	MAIN CPU Request Signal	JTL
10	P30	0	VS-1 PSO		30	P63	0	RAM A8 Signal Output	
11	P31	0	VS-1 PS1		31	P50	1/0	DATA BUS (D0)	
12	P32	0	VS-1 PS2		32	P51	1/0	DATA BUS (D1)	
13	P33	0	VS-1 PS3		33	P52	1/0	DATA BUS (D2)	
14	P70	0	RAM OD Signal Output		34	P53	1/0	DATA BUS (D3)	
15	P71	_0	HD61602 CS Signal Output		35	P40	1/0	DATA BUS (D4)	
16	P72	0	RAM and HD61602 R/W		36	P41	1/0	DATA BUS (D5)	
17	P73	0	RAM CS1 Signal Output		37	P42	1/0	DATA BUS (D6)	
18	RESET		Reset Input		38	P43	1/0	DATA BUS (D7)	
19	CL1		Clock		39	Vss		GND	
20	VDD		Power Supply		40	EVENT		GND	

Table 8 μ PD7508H-056 Terminal functions (Control unit IC3)

CIRCUIT DESECRIPTION

TONE unit (X52-1330-20)

Incoming serial data from the Control unit is converted to parallel data by IC2: MB88306. Serial data on the DT line, and the serial clock is on the CT line. When the ET terminal is high data is transferred, the tone output can be disabled by setting D1 to logic low. This can also be done by switching the ME terminal high.

IC1 : S7116A prevents the tone generator from oscillating outside the specified limits.

MODEM unit (X57-1140-20)

The modem Q3: μ PD65003C-20 uses a clock frequency of 3.6864MHz that is supplied across pins 18 and 19. Pin 13 is used to select transmit or receive operation of the modem. Transmit is selected when pin 13 is high, and receive when it is low. Pin 12 supplies the modem clock (1200Hz) to the MC terminal for transmit and receive clock timing purposes. Data transfers are based on this clock timing. When the transmit mode has been selected data is transferred to pins 1 thru 5 to produce the 1200 or 1800Hz MSK signal. This signal is then digital to analog converted by a ladder resistor network and applied to the TD terminal.

When the MODEM unit is in the receive mode it processes the signal applied to pin 9. This digitized Audio Frequency signal is received from the RD terminal after passing thru a band-pass filter and comparator circuit. This signal is converted by the MODEM unit and transferred thru pin 7, shaped by the LPF and comparator and applied again to pin 8.

The receive data and clock signal that are generated by the modern are based on the data applied to pins 12 and 14.

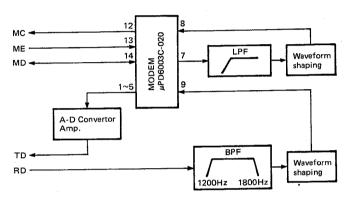


Fig. 17 MODEM unit (X57-1140-20) block diagram

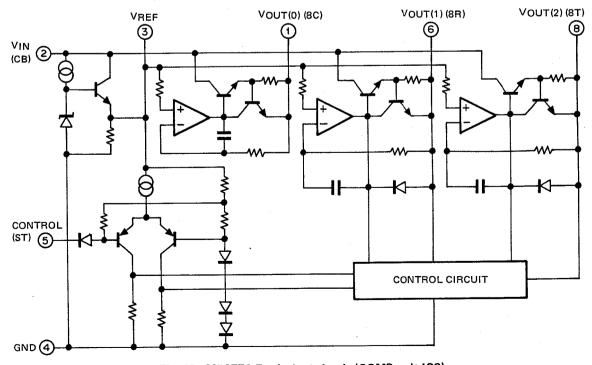


Fig. 18 MB3756 Equivalent circuit (COMP unit IC3)

CIRCUIT DESECRIPTION

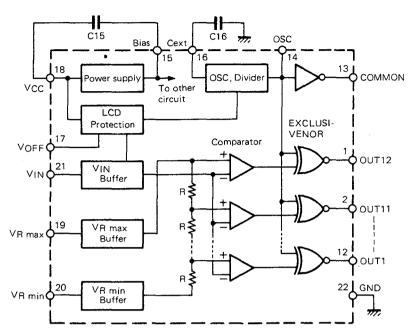


Fig. 19 IR2429 Block diagram (Display unit IC1)

Pin No.	Pin Name	1/0	Pull U/D	Part Name	Pin No.	Pin Name	1/0	Pull U/D	. Remarks		
3	S10					10					
5	S8					8					
7	S6]			1	6					
8	S5	0		LCD	-	5			Level meter segment output		
9	S4]	_	LCD		4					
10	\$3]				3					
11	S2]		Ì	1	2	1				
12	S1					1	1				
15	BIAS								Prevents C12 oscillation		
16	С			GND				,	External clock is used; C is grounded.		
18	VCC	1	_	Connector J6		8C2	T		8V power supply		
19	VREF-MAX								Determines level 10 voltage value.		
20	VREF-MIN]		GND			I		Determines level 1 voltage value.		
21	VIN	li	D R21	Connector J6		М	1		Meter input (DC)		
22	GND		_	GND					GND pin		

Note: Pins not specified must remain open.

Table 9 IR2429 Terminal functions

PARTS LIST

CAPACITORS

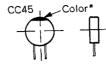
CC 45 TH 1H 220 J 1 2 3 4 5 6

1 = Type ceramic, electrolyic, etc. 4 = Voltage rating

2 = Shaperound, square, etc.

6 = Tolerance

5 = Value



Capacitor value

0 1 0 = 1pF

0 0 = 10pF

1 0 1 = 100pF

1 0 $3 = 0.01 \mu F$

2 2 0 = 22pF 1st number | Multiplier 2nd number

3 = Temp. coefficient Temperature Coefficient

	- 10111poru							
ſ	1st Word	С	L	P	R	S	T	U
ł	Color*	Black	Red	Orange	Yellow	Green	Blue	Violet
Ì	ppm/°C	0	-80	-150	-220	-330	-470	750

1 0 2 = $1000pF = 0.001\mu F$

2nd Word	G	Н	J	K	L
ppm/°C	± 30	± 60	± 120	± 250	± 500

Example CC45TH = -470±60 ppm/°C

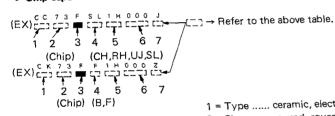
■ 1010	Brance										
Code		ב	G	J	K	M	X	Z	P		No code
			+ 2	. =	± 10	± 20	+ 40	+ 80	+ 100	More	10µF-10~+50
(%)	± 0.25	± 0.5	± 2	± 5	1 10	- 20	20	ì	1	Less	$4.7\mu F - 10 \sim +75$
ł		1				ļ	-20	20	0	than	$4.7\mu = 10^{-4.75}$

Code	В	С	D	F	G
(pF)	± 0.1	± 0.25	± 0.5	±1	± 2

Less than 10 pF

Katin	ig voirage	·			,				T	Ţ	
2nd word	А	В	С	D	E	F	G	Н	J	К	٧
word \	1.0	1.25	1.6	2.0	2.5	3.15	4.0	5.0	6.3	8.0	
		+	16	20	25	31.5	40	50	63	80	35
1	10	12.5		+		-		500	630	800	_
2	100	125	160	200	250	315	400				
3	1000	1250	1600	2000	2500	3150	4000	5000	6300	8000	
1	·								m		

• Chip capacitors



• Chip resistor (Carbon)

Carbon resistor (Normal type)



- 1 = Type ceramic, electrolytic, etc.
- 2 = Shape round, square, etc.
- 3 = Dimension
- 4 = Temp. coefficient
- 5 = Voltage rating
- 6 = Value
- 7 = Tolerance.

Dimension

Dimension code	L	W	T
Empty	5.6 ± 0.5	5.0 ± 0.5	Less than 2.0
Linpty	3.2 ± 0.2	1.6 ± 0.2	Less than 1.25
	2.0 ± 0.3	1.25 ± 0.2	Less than 1.25
i .	2.0		

Dimension

Dimension code	L	w	T	Wattage
E	3.2 ± 0.2	1.6 ± 0.2	0.57	2B
F	2.0 ± 0.3	1.25 ± 0.2	0.45	2A

Rating wattage

			•				
1	Cord	W	attage	Cord	Wattage	Cord	Wattage
	2A	1	10W	2E	1 4W	3A	1W
į	2B	1	8W	2H	1 2W	3D	2W
	2C	1	6W				



PARTS LIST

SEMICONDUCTOR

N : New parts

* : Please note that parts are sometimes not in stock and it takes much time to deliver.

Item	Re- marks	Part No.
Diode		1N60 1N60A 1N4448
		1S1555 1S1587 1S2208 1SS106 1SS133
		MA856 MC911 MC921 MI308 MI407
		U15B UM9401
Chip diode	2 2	1SS181 1SS184
Vari-cap		1SV50
Zener diode		MTZ5.6JC MTZ6.2JA MTZ7.5JA MTZ11JC
LED	N	LN38GPL LN222RP LN322GP LN442YP
	N	U19DD306
LCD	N	FSS8066
Thermister		112-502-2
TR	N	2SA790(A) 2SA1015(Y) 2SA1048(Y)
		2SC496(Y) 2SC1775(E) 2SC2347 2SC2458(Y) 2SC2538-22-A 2SC2668(Y) 2SC3019

Item	Re- marks	Part No.
		2SD1406(Y)
Chip TR		2SC2712(BL) 2SC2712(K) 2SC2712(Y) 2SC2714(Y)
Digital TR		2SC3295(B)
		DTA114YF
		DTC114ES DTC124EF DTC143TS
	N	UN2213
FET		2SK30A(O) 2SK125 2SK192A(GR)*P
		3SK74(L) 3SK129(S)
Power module		M57726 M57737
ıc	N	HD61602
		IR2429
		LR4087
		MB3756 MC14584BCP MC145151P MC145155P*K
		NE555P
		NJM78M06A NJM4558M
		PST520D
	N	TA7761P TC40H374P TC5047AP-1 TMP47C46N-9042
	N	μPC1241H μPD7508HC-056

	TM-2550A (K1,M1,M3)	TM-2550E (T,W)	TM-2530A (K2,M2,M4)
FINAL UNIT	X45-1440-10	X45-1440-10	X45-1440-11
PLL UNIT	X50-2030-10	X50-2030-51	X50-2030-11
	X53-1440-10 (K1,M1)	X53-1440-51 (T)	X53-1440-10 (K2,M2)
CONTROL UNIT	X53-1440-21 (M3)	X53-1440-61 (W)	X53-1440-21 (M4)
DISPLAY UNIT	X54-1860-10	X54-1860-10	X54-1860-10
COMPOUND UNIT	X60-1280-10	X60-1280-51	X60-1280-10

→ New Parts

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Parts without Parts No. are not supplied.

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Ref. No.	Address	New Parts	Parts No.	Description	Desti- nation			
参照番号	位 置	新	部品番号	部品名/規格	仕 向			
TM-2550A/E, TM-2530A GENERAL								
1 2 3 3 4	16,2F 36 2B,3F 2B 2A	* * * * *	A01-0996-02 A01-0997-02 A20-2550-03 A20-2559-03 A21-0791-04	METALLIC CABINET (T0P) METALLIC CABINET (B0TT0M) PANEL ASSY PANEL ASSY DRESSING PANEL (KEYB0ARD)	K1,K2,M1-M4 T1,W1			
5 - -	2A	* * *	A21-0795-04 A13-0666-02 A13-0667-02 A13-0668-04	DRESSING PANEL (DCL) MOUNTAIN BRACKET ASSY (R) ACSY MOUNTAIN BRACKET ASSY (L) ACSY MOUNTAIN HARDWARE				
10 11 12 13	2F.2G 1F 1F 2F	* *	B11-0436-04 B01-0655-03 B04-0411-04 B05-0708-04 B10-0675-04	REFLECTION GLASS (DCS,CSQ) SIDE ESCUTCHEON SP METAL SP SARAN NET FRONT GLASS				
14 14 14 15	36 36 36 2A 2A 2A	* * * *	B40-3610-04 B40-3611-04 B40-3619-04 B43-1057-04 B43-1058-04	MODEL NAME PLATE MODEL NAME PLATE MODEL NAME PLATE BADGE BADGE	K1,M1,M3 T1,W1 K2,M2,M4 K1,M1,M3 T1			
15 15 - -	2A 2A	* * * *	B43-1059-04 B43-1060-04 B11-0429-04 B11-0430-04 B11-0431-04	BADGE BADGE LIGHT GUIDING PLATE (AL) LIGHT GUIDING PLATE (P. MR) LIGHT GUIDING PLATE (F. LØCK)	W1 K2,M2,M4 T1,W1			
- - -		* * *	B11-0432-04 B11-0433-04 B11-0434-04 B46-0410-00 B50-8039-00	LIGHT GUIDING PLATE (PH®NE) LIGHT GUIDING PLATE (MAIN) REFLECTION GLASS(ON AIR/F.LOCK WARRANTY CARD INSTRUCTION MANUAL	K1,K2,M1-M4 K1,K2 K1,K2,M1-M4,W1			
-		*	B50-8040-00	INSTRUCTION MANUAL	T1			
		*	E30-2022-15	DC CORD ASSY (ACSY)				
20 21 - -	26 10		F10-1206-04 F15-0649-04 F05-1031-05 F05-8021-05 F20-0521-04	GROUNDING SPRING SHADE SHEET (KEYBOARD PCB) FUSE (10A) ACSY FUSE (8A) ACSY INSULATE PLATE (B)LITHIUM BTRY	K1,M1,M3,T1,W1 K2,M2,M4			
26 27 28 -	1C	* *	G01-0818-04 G02-0505-05 G10-0642-04 G11-0616-04 G13-0823-04	CØMPRESSIØN SPRING (KNØB) KNØB FITTING SPRING (AF/SQ) SHADE CLØTH SHADE SHEET (F. LØCK) CUSHIØN (MØUNTING BRACKET ACSY				
- - - -		* * *	H01-4671-03 H01-4672-03 H01-4673-03 H01-4674-03 H03-2268-04	ITEM CARTON BOX (INSIDE) ITEM CARTON BOX (INSIDE) ITEM CARTON BOX (INSIDE) ITEM CARTON BOX (INSIDE) OUTER PACKING CASE	K1,M1,M3 T1 W1 K2,M2,M4 K1,M1,M3			
- - - -		* *	H03-2269-04 H03-2270-04 H10-2501-03 H10-2608-02 H10-2609-02	BUTER PACKING CASE BUTER PACKING CASE PBLYSTYRENE FBAMED FIXTURE PBLYSTYRENE FBAMED FIXTURE PBLYSTYRENE FBAMED FIXTURE	T1,W1 K2,M2,M4 K1,M1,M3,T1,W1 K2,M2,M4			

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		H25-0029-04 H25-0103-04 H25-0106-04 H25-0116-04 H25-0117-04	PROTECTION BAG (SCREW, NUT ETC) PROTECTION BAG (MIC, MNT BRKT) PROTECTION BAG PROTECTION BAG (ACSY) PROTECTION BAG (DC CORD)	
1F 1C 1B,2B	*	J21-1144-24 J21-4182-04 J29-0409-04 J02-0439-05 J19-1346-04	SP MOUNTING HARDWARE (KEY/PC) MOUNTING HARDWARE SW KNOBS GUIDE (KNOB) FOOT (ACSY) MIC HOOK (ACSY)	
		J61-0408-05	WIRE BAND	
3F 3F 2A 2A	* * * *	K21-0779-05 K23-0779-04 K29-3039-05 K29-3044-05	KN®B (MAIN) KN®B (AF,50) KN®B ASSY (PUSH SW) KN®B ASSY (TACT SW)	
		N14-0526-14 N09-0008-04 N09-0632-05 N14-0510-04 N15-1040-45	RBUND NUT (VBL) HEX HEAD SCREW (ACSY) TAPTITE SCREW (ACSY) FLANGE NUT (ACSY) FLAT WASHER	
		N15-1060-46 N16-0060-46 N87-4008-41 N99-0304-04 N09-0697-05	FLAT WASHER (ACSY) SPRING WASHER (ACSY) BRAZIER TAPTITE SCREW (ACSY) SCREW WITH HEX HØLE (MNT BRKT) SEMUS SCREW (PANEL)	
		N29-0301-04	STOPPER RING (TACT KNOB)	
		550-1406-05	TAKT SW FOR MICROHONE(UP/DOWN)	
1F	*	T07-0240-05 T91-0331-05 T91-0335-05	LØUDSPEAKER(FULLRANGE) MICRØPHØNE (ACSY) MICRØPHØNE (ACSY)	K1,K2,M1-M4,W1 T1
10	*	WO2-0371-05 WO1-0401-05 WO9-0326-05	ROTARY ENCODER HEX WRENCH (ACSY) LITHIUM BATTERY(BR2032)	
2D,2G 2D,2G 2G 2G 2G	* * *	X45-1440-10 X45-1440-11 X50-2030-10 X50-2030-11 X50-2030-51	FINAL UNIT (TM-255DA/E) FINAL UNIT (TM-253DA) PLL UNIT (TM-255DA) PLL UNIT (TM-253DA) PLL UNIT (TM-255DE)	K1,M1,M3,T1,W1 K2,M2,M4 K1,M1,M3 K2,M2,M4 T1,W1
3F 3F 3F 3F 1B,1C	* * * *	X53-1440-10 X53-1440-21 X53-1440-51 X53-1440-61 X54-1860-10	CONTROL UNIT CONTROL UNIT CONTROL UNIT CONTROL UNIT DISPLAY UNIT	K1,K2,M1,M2 M3,M4 T1 W1
2G 2G	*	X60-1280-10 X60-1280-51	COMPOUND UNIT	K1,K2,M1-M4 T1,W1
		CC45SL2H0B0D CC45SL2H150J CC45SL2H050C CC45SL2H060D CK45B2H102K	CERAMIC 8. OPF D CERAMIC 15PF J CERAMIC 5. OPF C CERAMIC 6. OPF D CERAMIC 1000PF K	K2,M2,M4 K1,M1,M3,T1,W1 K2,M2,M4
	位 置 1F 1C 1B,2B 3F 3F 2A 2A 1F 1C 2D,2G 2G 2G 2G 2G 3F 3F 3F 3F 1B,1C 2G 2G 2G	位 置 Parts 新 1F ** 1C ** 2D,2G ** 2A ** 1F ** 1C ** 2D,2G ** 3F 3F ** 3F 3F ** 3F 3F ** 2A **	### ### ### ### ### ### ### ### #######	### ### ### ### ### #### ############

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Ref. No.	Address		Parts No.	Des	cription	Desti- nation
参照番号	位 置	Parts 新	部品番号	部品	名 / 規 格	仕 向
C5 C6 C7 -9 C10 C11 ,12			CC45SL2H22OJ CC45CH2H01OC CC45SL2H22OJ CC45CH1H01OC CC45SL2H1OOD	CERAMIC CERAMIC CERAMIC	22PF J 1.OPF C 22PF J 1.OPF C 1OPF D	
C13 C14 C15 C16 -19 C21 -25			CE04W1C220M CK45B1H102K CE04W1C220M CK45B1H102K CK45B1H102K	CERAMIC ELECTRO CERAMIC	22UF 16WV 1000PF K 22UF 16WV 1000PF K 1000PF K	
C26 C27 C28 C29 C30 -34			CS15E1C3R3M C91-0667-05 CK45B1H102K C91-0667-05 CK45B1H102K	CERAMIC CERAMIC CERAMIC	3.3UF 16WV 0.0047UF K 1000PF K 0.0047UF K 1000PF K	
100 101 102	3E 2D 3D,2G		E04-0161-05 E11-0401-05 E30-2021-25	M TYPE RECEPT EAR PHONE JACK POWER SUPPLY (K	
105 105 F1 F1	2E,1G 2E,1G	*	F01-0931-05 F01-0935-05 F05-1031-05 F05-8021-05	HEAT SINK HEAT SINK FUSE FUSE	(10A) (8A)	K1,M1,M3,T1,W1 K2,M2,M4 K1,M1,M3,T1,W1 K2,M2,M4
108 109	1 D 3D	*	J11-0409-05 J41-0017-05	WIRE CLAMPER BUSHING	(CORD)	
L1 L2 L3 L4 L5			L34-1020-05 L34-0908-05 L34-0692-05 L34-0452-05 L34-0908-05	COIL COIL COIL COIL	(ø3,3.5) (ø3,3.5T) (ø5,4T) (ø3,6T) (ø3,3.5T)	
L6 L7 L8 ,9			L34-0742-05 L34-0823-05 L40-1092-14	COIL COIL SMALL FIXED I	(ø3,5T) (ø5,3T) NDUCT®R (1UH)	
M N			N09-0623-04 N09-0626-04	SCREW SCREW	(SEMUSE) (SEMUSE)	
R2 R2 VR1 VR2			R92-0150-05 RD14DB2H151J RD14DB2H181J R12-0434-05 R12-4417-05	SMALL-RD	O 0HM 150 J 1/2W 180 J 1/2W (1000HM) (50K0HM)	K1,M1,M3,T1,W K2,M2,M4
VR3 VR4			R12-3455-05 R12-4417-05	TRIMMING POT. TRIMMING POT.	(10K0HM) (50K0HM)	
D1 D2 D2 D3 D4 ,5			U15B MI407 UM9401 MI308 151587	DIØDE DIØDE DIØDE DIØDE DIØDE		K2,M2,M4 K1,M1,M3,T1,W
01 01 02 03 04 -7			M57726 M57737 2SD1406(Y) 2SA1015(Y) 2SC2458(Y)	POWER MODULE POWER MODULE TRANSISTOR TRANSISTOR TRANSISTOR		K1,M1,M3,T1,V K2,M2,M4

× New Parts

PARTS LIST

Parts without Parts No. are not supplied.
Les articles non mentionnes dans le Parts No. ne sont pas fournis.
Telle ohne Parts No. werden nicht geliefert.

Ref. No.	Address Nev		Description	Desti- nation
参照番号	位置新		部品名/規格	t 向
	PLL UNIT (X50-2030-XX) -10:	K1,M1,M3 -11 : K2,M2,M4 -51 : T1	,W1
C1 C2 C4 C5 C6 • 7		C91-0117-05 CS15E1VR47M CF92V1H683J C91-1008-05 CS15E1C2R2M	CERAMIC 0.01UF K TANTAL 0.47UF 35WV MF 0.068UF J CERAMIC 0.011UF K TANTAL 2.2UF 16WV	
C8 C9 ,10 C11 C12 C13		CQ92M1H393K CS15E1E010M CK45B1H102K CC45CH1H150J CC45CH1H0R5C	MYLAR 0.039UF K TANTAL 1.0UF 25WV CERAMIC 1000PF K CERAMIC 15PF J CERAMIC 0.5PF C	
C14 C15 C16 C17 C18		CC45CH1H100D CC45CH1H080D CC45CH1H030C CC45CH1H100D C91-0117-05	CERAMIC 10PF D CERAMIC 8. 0PF D CERAMIC 3. 0PF C CERAMIC 10PF D CERAMIC 0. 01UF K	
C19 C20 C21 C22 C23		CE04W1A470M CC45CH1H02OC CC45CH1H04OC CK45B1H1O2K CE04W1A470M	ELECTR® 47UF 10WV CERAMIC 2. OPF C CERAMIC 4. OPF C CERAMIC 1000PF K ELECTR® 47UF 10WV	
C24 C25 C26 C27 C28 ,29		CC45CH1H330J CK45B1H102K CE04W1A470M C91-0117-05 CK45B1H102K	CERAMIC 33PF J CERAMIC 1000PF K ELECTR® 47UF 10WV CERAMIC 0.01UF K CERAMIC 1000PF K	
030 031 ,32 033 035 036		C91-0117-05 CK45B1H102K C91-0117-05 CE04W1C100M C91-0117-05	CERAMIC 0.01UF K CERAMIC 1000PF K CERAMIC 0.01UF K ELECTRN 10UF 16WV CERAMIC 0.01UF K	
C37 ,38 C39 ,40 C41 C42 C43		CK45B1H102K C91-0117-05 CE04W1H4R7M C91-0667-05 CK45B1H471K	CERAMIC 1000PF K CERAMIC 0.01UF K ELECTRO 4.7UF 50WV CERAMIC 0.0047UF K CERAMIC 470PF K	
C44 646 C47 C48 C50		CC45CH1H270J CC45CH1H020C C91-0765-05 CK45B1H102K CQ92M1H223K	CERAMIC 27PF J CERAMIC 2. OPF C CERAMIC 0. 0047UF M CERAMIC 1000PF K MYLAR 0. 022UF K	
C51 ,52 C53 C54 C55 C57		CS15E1C2R2M CEO4W1A47OM C91-O117-O5 CK45B1H1O2K CEO4W1A47OM	TANTAL 2.2UF 16WV ELECTR® 47UF 10WV CERAMIC 0.01UF K CERAMIC 1000PF K ELECTR® 47UF 10WV	
C58 C59 C60 C61 C62		CK45B1H102K CC45CH1H020C CC45CH1H12OJ CC45CH1H100D CC45CH1H07OD	CERAMIC 1000PF K CERAMIC 2.0PF C CERAMIC 12PF J CERAMIC 10PF D CERAMIC 7.0PF D	
C63 C64 C65		CE04W1A101M C91-0117-05 CC45CH1H030C	ELECTR® 100UF 10WV CERAMIC 0.01UF K CERAMIC 3.0PF C	

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参照番号	位置新	部品番号	部品名/規格	仕 向
C66 C67 C68 •69 C70 C71		CC45CH1H120J CC45CH1H030C CK45B1H102K CC45CH1H330J CC45CH1H040C	CERAMIC 12PF J CERAMIC 3.0PF C CERAMIC 1000PF K CERAMIC 33PF J CERAMIC 4.0PF C	
C72 C73 C74 C75 C76		CK45B1H102K CK45B1H471K CS15E1E010M C91-0757-05 CC45CH1H330J	CERAMIC 1000PF K CERAMIC 470PF K TANTAL 1.0UF 25WV CERAMIC 0.001UF K CERAMIC 33PF J	
C77 C78 ,79 C8D C81 ,82 C83		CE04W1A470M C91-0117-05 CK45B1H471K CC45CH1H270J CK45B1H102K	ELECTRN 47UF 10WV CERAMIC 0.01UF K CERAMIC 470PF K CERAMIC 27PF J CERAMIC 1000PF K	
C84 C85 C86 C87 C88		CC45CH1H030C CC45CH1H0R5C C91-0117-05 CC45CH1H040C CC45CH1H180J	CERAMIC 3.OPF C CERAMIC 0.5PF C CERAMIC 0.01UF K CERAMIC 4.OPF C CERAMIC 18PF J	
C89 C90 ,91 C92 C92 C93		CC45CH1H220J C91-0117-05 CC45CH1H070D CC45CH1H121J C91-0117-05	CERAMIC 22PF J CERAMIC 0.01UF K CERAMIC 7.0PF D CERAMIC 120PF J CERAMIC 0.01UF K	T1,W1 K1,K2,M1—M4
C95 C96 C97 C99 C100		CK45B1H102K CK45B1H102K C91-0117-05 CC45CH1H270J CC45SL1H101J	CERAMIC 1000PF K CERAMIC 1000PF K CERAMIC 0.01UF K CERAMIC 27PF J CERAMIC 100PF J	T1,W1
C101 C102 TC1 TC2 ,3 TC4		CC45CH1H070D CC45CH1H22OJ CO5-0062-05 CO5-0030-15 CO5-0062-05	CERAMIC 7. DPF D CERAMIC 22PF J TRIMMING CAP (6PF) TRIMMING CAP (2DPF) TRIMMING CAP (6PF)	K1,M1,M3,T1,W1
TC5		CO5-0067-05	TRIMMING CAP (25PF)	T1,W1
		E04-0154-05	RF COAXIAL CABLE RECEPTACLE	
L1 L2 L3 L4 L5	*	L40-3391-03 L32-0682-05 L40-1021-03 L34-0894-05 L34-0893-05	SMALL FIXED INDUCTOR (3.3UH) OSCILLATING COIL (3.5T) SMALL FIXED INDUCTOR (1UH) COIL (5T) COIL (4T)	
L6 L7 L8 L9 L10		L34-0894-05 L34-0893-05 L40-3391-14 L40-3391-03 L32-0664-05	C0IL (5T) C0IL (4T) SMALL FIXED INDUCTOR (3.3UH) SMALL FIXED INDUCTOR (3.3UH) 0SCILLATING C0IL (4.5T)	
L11 L12 L13 L14 ,15 L16		L40-1092-14 L34-1025-05 L40-3391-14 L34-2035-05 L40-4701-14	SMALL FIXED INDUCTOR (1UH) COIL (5.5T) SMALL FIXED INDUCTOR (3.3UH) COIL (TUNING) SMALL FIXED INDUCTOR (47UH)	T1,W1
L17	*	L32-0681-05	0SCILLATING COIL	

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L18			L77-1290-05	CRYSTAL RES®NAT®R (39.48MHZ)	
R39 R39 R94		*	RD14DB2H22OJ RD14DB2H33OJ R9O-O6OO-O5	SMALL-RD 22 J 1/2W SMALL-RD 33 J 1/2W CAPACITOR BLOCK	K1,M1,M3,T1,W1 K2,M2,M4
D1 D2 D3 D4 D5			151555 16V50 152208 151555 1SV50	DINDE VARI CAP DINDE DINDE VARI CAP	
D6 D7 IC1 IC2 Q1			MTZ6.2JA MA856 MC145151P MC145155P*K 2SC2458(Y)	ZENER DIØDE DIØDE IC IC TRANSISTØR	T1,W1
Q2 Q3 -5 Q6 ,7 Q8 Q9			25A1048(Y) 2SC1775(E) 2SC2668(Y) 2SK125 2SC2458(Y)	TRANSISTØR TRANSISTØR TRANSISTØR FET TRANSISTØR	
Q10 Q11 Q12 Q12 Q13			2SC2668(Y) 2SC2347 2SC2538-22-A 2SC3019 2SK30A(N)	TRANSISTÖR TRANSISTÖR TRANSISTÖR TRANSISTÖR FET	K2,M2,M4 K1,M1,M3,T1,W1
014 ,15 016 017 -22 023			25C2458(Y) 25K192A(GR)*P 25C2668(Y) 25C2458(Y)	TRANSISTØR FET TRANSISTØR TRANSISTØR	T1,W1
CONT	ROL UNI	T (>	(53-1440-XX) -10 :	K1,K2,M1,M2 -21: M3,M4 -51: T1	-61 : W1
C1 C2 C3 C4 C5			CK45B1H102K CE04CW1A330M CE04W1A101M C91-1008-05 CE04W1A470M	CERAMIC 1000PF K ELECTR® 33UF 10WV ELECTR® 100UF 10WV CERAMIC 0.022PF ELECTR® 47UF 10WV	K1,K2,M1-M4 K1,K2,M1-M4
C6 C7 C8 C9 C10 -16			CS15E1A100M CK45B1H681K CK45B1H102K CK45B1H182K C91-1008-05	TANTAL 10UF 10WV CERAMIC 6BOPF K CERAMIC 1000PF K CERAMIC 1BOOPF K CERAMIC 0.022PF	
C17 C18 C19 ,20 C23 ,24 C25 -28			CEO4W1E3R3M CEO4W1H010M C91-1008-05 C91-1008-05 CK45B1H102K	ELECTRO 3.3UF 25WV ELECTRO 1.0UF 50WV CERAMIC 0.022PF CERAMIC 0.022PF CERAMIC 1000PF K	K1,K2,M1-M4
L1 L2 L3			L79-0013-05 L79-0012-05 L78-0013-05	RESONATOR (4.19MHZ) RESONATOR (3.58MHZ) RESONATOR (4.19MHZ)	K1,K2,M1—M4
R8 R16 R19 R20			R90-0202-05 R90-0286-05 R90-0233-05 R90-0281-05 R90-0291-05	MULTI-C9MP 47KX4 J 1/6W MULTI-C9MP 4.7KX4 J 1/6W MULTI-C9MP 10KX4 J 1/6W MULTI-C9MP 10KX6 J 1/6W MULTI-C9MP 10OKX4 J 1/6W	
R21	ľ				

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参照番号	位置 新	部品番号	部 品 名 / 規 格	nation 仕 向
R35 VR1		R90-0594-05 R12-7408-05	MULTI-COMP TRIMMING POT. (500KOHM)	
D1 D2 D2 D3 D4 -8		MC911 1N4448 1S1555 MTZ7.5JA 1N4448	DIODE DIODE DIODE ZENER DIODE DIODE	
D4 -8 D9 D9 D10 D10		1S1555 1N4448 1S1555 1N4448 1S1555	D10DE D10DE D10DE D10DE	K1,K2,M1,M2,W1 K1,K2,M1,M2,W1 T1,W1 T1,W1
D11 D11 D12 D12 D13 -16		1N4448 1S1555 1N4448 1S1555 1N4448	DIADE DIADE DIADE	T1,W1 T1,W1 K1,K2,M1—M4
D13 -16 D17 -21 D17 -21 D22 D22		151555 1N4448 151555 1N4448 151555	DIODE DIODE DIODE DIODE	K1,K2,M1-M4 K1,K2,M1-M4 K1,K2,M1-M4
D23 D24 D24 D25 ,26 D27		MTZ5. 6JC 1N4448 1S1555 1SS106 1N4448	ZENER DIØDE DIØDE DIØDE DIØDE DIØDE	K1,K2,M1—M4
D27 D28 D28 D29 IC1		1S1555 1N4448 1S1555 1SS133 MC14584BCP	DIQDE DIQDE DIQDE DIQDE IC	K1,K2,M1—M4 K1,K2,M1—M4
IC2 IC3 IC4 IC5 IC6		TMP47C46N-9042 UPD7508HC-056 LR4087 TC40H374P TC5047AP-1	IC(CPU) IC(CPU) IC IC IC IC	K1,K2,M1—M4
107 01 02 03 04 ,5		PST520D DTC124EF 2SC2458(Y) DTA114YF DTA114YF	IC(LØW PØWER RESET) DIGITAL TRANSISTØR TRANSISTØR DIGITAL TRANSISTØR DIGITAL TRANSISTØR	K1,K2,M1—M4 K1,K2,M1—M4
Q6		DTC143TY	DIGITAL TRANSISTOR	K1,K2,M1—M4
C1 "	Г	DISPLAY U	NIT (X54-1860-10) CERAMIC 1000PF K	
C1 -7 C8 C9 -11 C12 -14		CK45B1H102K CE04CW1C4R7M CK45B1H102K CK73FB1H102K	CERRINIC 1000FF K ELECTR® 4.7UF 16WV CERAMIC 1000PF K CHIP C 1000PF K	
		E06-0858-05	8P MIC CONNECTOR	
-		J61-0408-05	WIRE BAND	
VR1	*	R92-0150-05 R05-3436-05	JUMPER REST O ØHM PØTENTIØMETER(10KØHM WITH SW)	

× New Parts

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参照番号	位 置	Parts 新	部品番号	部品名。	/ 規 格		nation 仕 向
VR2		*	R05-4415-05	POTENTIOMETER (50KB) SQ		
S1 -3 S4 .5 S6 .7 S8		*	\$59-0435-05 \$40-2443-05 \$40-2444-05 \$40-2443-05 \$40-2444-05	PUSH SWITCH (PUSH SWITCH ()	LØCK) NØN-LØCK LØCK)		
S9 -16			S50-1426-05	SENSITIVE SWITCH	(TACT)		
- - - D1 -5		* * * *	FSS8066 HD61602 UN2213 U19DD306 1N4448	IC () DIGITAL TR ()	KEYBOARD KEYBOARD KEYBOARD KEYBOARD	ASSY) ASSY)	
D1 -5 D6 ,7 D8 ,9 D8 ,9 D10 -13		*	1S1555 MC921 1N4448 1S1555 LN38GPL	DIQDE DIQDE DIQDE LED			
D14 D15 D16 .17 IC1 Q1			LN222RP LN322GP LN442YP IR2429 DTC124EF	LED (RED) GREEN) YELLØW)		
02		*	2SA790(A)	TRANSISTOR			
				. (X59-1000-10)			
R1 R2 R3 R4 R5			RK73FB2A682J RK73FB2A221J RK73FB2A104J RK73FB2A153J RK73FB2A333J	CHIP R 6.80 CHIP R 220 CHIP R 1000 CHIP R 15K CHIP R 33K	K J J	1/10W 1/10W 1/10W 1/10W 1/10W	
R6 -8 R9 R10 R11 R12			RK73FB2A224J RK73FB2A474J RK73FB2A153J RK73FB2A562J RK73FB2A184J	CHIP R 220 CHIP R 470 CHIP R 15K CHIP R 5.6 CHIP R 180	K J J K J	1/10W 1/10W 1/10W 1/10W 1/10W	
R13 R14			RK73FB2A224J RK73FB2A474J	CHIP R 220 CHIP R 470		1/10W 1/10W	
C1 C2 C3 C4 C5			CC73FSL1H390J CK73FB1H152K CC73FSL1H331K CC73FSL1H561K CC73FSL1H331K	CHIP C 39PC CHIP C 0.00 CHIP C 3300 CHIP C 3300 CHIP C 3300	015UF PF PF		
D1 D2 IC1 Q1		* *	199184 199181 NJM4558M 2902712(Y)	CHIP DIODE CHIP DIODE IC(OP AMP X2) CHIP TR			
			MIC AMP., S-W	ETER (X59-1010-10)			
R1 R2 R3 R4 R5			RK73FB2A473J RK73FB2A474J RK73FB2A473J RK73FB2A474J RK73FB2A224J	CHIP R 47K CHIP R 47D CHIP R 47K CHIP R 47D CHIP R 47D CHIP R 220	K J J K J	1/10W 1/10W 1/10W 1/10W 1/10W	
R6			RK73FB2A184J	CHIP R 180	K J	1/10W	

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R7 RB R9 R10		RK73FB2A473J RK73FB2A472J RK73FB2A103J RK73FB2AB22J	CHIP R 47K J 1/10W CHIP R 4.7K J 1/10W CHIP R 10K J 1/10W CHIP R 8.2K J 1/10W	
C1 D1 D2 IC1		CK45FB1H472K 1SS184 1SS181 NJM4558M	CHIP DINDE CHIP DINDE IC(NP AMP X2)	
4 12 12			NT CH. (X59-1020-10)	
R1 R2 R3 R4 R5 -7		RK73FB2A103J RK73FB2A472J RK73FB2A223J RK73FB2A273J RK73FB2A103J	CHIP R 10K J 1/10W CHIP R 4.7K J 1/10W CHIP R 22K J 1/10W CHIP R 27K J 1/10W CHIP R 10K J 1/10W	
C1 D1 Q14		CK73FB1H102K 1SS181 2SC2712(Y)	CHIP C 0.001UF CHIP DIØDE CHIP TRANSISTØR	
		·	CTOR (X59-1030-10)	
R1 R2 R3 R4 R5 +6		RK73FB2A224J RK73FB2A222J RK73FB2A332J RK73FB2A333J RK73FB2A104J	CHIP R 220K J 1/10W CHIP R 2.2K J 1/10W CHIP R 3.3K J 1/10W CHIP R 33K J 1/10W CHIP R 100K J 1/10W	
R7 R8 R9 ,10 R11 R12		RK73FB2A563J RK73FB2A102J RK73FB2A103J RK73FB2A102J RK73FB2A104J	CHIP R 56K J 1/10W CHIP R 1.0K J 1/10W CHIP R 10K J 1/10W CHIP R 1.0K J 1/10W CHIP R 1.0K J 1/10W	
R13 ,14		R92-0670-05	CHIPR O OHM	
C1 +2 C3 +4 C5 D1 IC1		CK73FB1H102K CK73FF1E473Z CK73FB1H102K 1SS1B1 NJM4558M	CHIP C 0.001UF CHIP C 0.047UF CHIP C 0.001UF CHIP DINDE IC(NP AMP X2)	
Q1		2SC2714(Y)	CHIP TRANSIST®R	
		SQUELCH CON	ITROL (X59-1040-10)	
R1 R2 R3 R4 R5		RK73FB2A103J RK73FB2A223J RK73FB2A682J RK73FB2A474J RK73FB2A472J	CHIP R 10K J 1/10W CHIP R 22K J 1/10W CHIP R 6.8K J 1/10W CHIP R 470K J 1/10W CHIP R 4.7K J 1/10W	
R6 R7 R8 R9 R10		RK73FB2A332J RK73FB2A682J RK73FB2A332J RK73FB2A393J RK73FB2A472J	CHIPR 3.3K J 1/10W CHIPR 6.8K J 1/10W CHIPR 3.3K J 1/10W CHIPR 39K J 1/10W CHIPR 4.7K J 1/10W	
R11		R92-0670-05	CHIPR O NHM	
C1 D1 Q1 Q2 -3 Q4		CK73FB1H102K 1SS184 2SC2712(Y) 2SC3295(B) 2SC2712(BL)	CHIP C 0.001UF CHIP DIØDE CHIP TRANSISTØR CHIP TRANSISTØR CHIP TRANSISTØR	
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Q5 Q6		25C2712(Y) 25C2712(BL)	CHIP TRANSIST®R CHIP TRANSIST®R	
С	OMPOUND (JNIT (X60-1280-XX)	-10: K1,K2,M1,M2,M3,M	4 -51 : T1,W1
C1 C2 C3 C4 C5		CC45CH1H33OJ CC45RH1H12OJ CC45CH1H03OC CC45RH1H1OOD CC45CH1H22OJ	CERAMIC 33PF CERAMIC 12PF CERAMIC 3. OPF CERAMIC 10PF CERAMIC 22PF	D D J
C6 C7 C8 C9 C10		CK45B1H102K C91-0117-05 CK45B1H102K C91-0117-05 CK45B1H102K	CERAMIC 1000PF CERAMIC 0.01UF CERAMIC 1000PF CERAMIC 0.01UF CERAMIC 1800PF	К К К К
C11 C12 C13 C14 C15 ,16		CC45CH1H01OC CC45CH1H15OJ CC45CH1H02OC CC45CH1H33OJ C91-0117-05	CERAMIC 1.0PF CERAMIC 15PF CERAMIC 2.0PF CERAMIC 33PF CERAMIC 0.01UF	C J K
C17 C18 C19 C20 .21 C22		CC45CH1H050C C91-0117-05 CC45SL1H101J C91-0117-05 C91-0757-05	CERAMIC S. OPF CERAMIC O. OIUF CERAMIC 100PF CERAMIC O. OIUF CERAMIC O. OOIUF	С К Ј К
C23 C24 C25 C27 C28 ,29		C91-0117-05 C092M1H473K CE04W1C102M CE04W1H010M CE04W1H470M	CERAMIC 0.01UF MYLAR 0.047UF ELECTR0 1000UF ELECTR0 1.0UF ELECTR0 47UF	K K 16WV 50WV 10WV
C31 C32 C33 C34 ,35 C36		CQ92M1H104K CE04W1A221M CE04W1A470M C91-0117-05 CC45SL1H101J	MYLAR 0.10UF ELECTR® 220UF ELECTR® 47UF CERAMIC 0.01UF CERAMIC 100PF	K 10WV 10WV K J
C37 C38 C39 C40 C41		CC45CH1H04OC CC45SL1H181J C91-0117-05 CE04W1A47OM CC45SL1H331J	CERAMIC 4. OPF CERAMIC 180PF CERAMIC 0. 01UF ELECTRØ 47UF CERAMIC 330PF	C J K 10WV J
C42 C43 C44 C45 C46		CK45B1H471K CK45B1H102K CC45CH1H330J C91-0667-05 CC45CH1H330J	CERAMIC 470PF CERAMIC 1000PF CERAMIC 33PF CERAMIC 0.0047UF CERAMIC 33PF	K J K J
C47 C48 C49 C50 ,51 C52 -54		C91-0117-05 CC45SL1H121J CK45B1H102K C91-0117-05 CF92V1H104J	CERAMIC 0.01UF CERAMIC 120PF CERAMIC 1000PF CERAMIC 0.01UF MF 0.10UF	K J K K J
C55 C56 C58 ,59 C60 C61		CK45B1H102K CE04W1H010M C91-0117-05 CE04W1C330M C91-0117-05	CERAMIC 1000PF ELECTR® 1.0UF CERAMIC 0.01UF ELECTR® 33UF CERAMIC 0.01UF	K 50WV K 16WV K
C62		CE04W1C100M	ELECTR® 10UF	16WV

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C63 C64 C65 C66 C67			CE04W1C330M C91-0117-05 CE04W1C100M C91-0117-05 CE04W1C100M	CERAMIC ELECTR® CERAMIC	33UF 16WV 0.01UF K 10UF 16WV 0.01UF K 10UF 16WV	
C68 C69 C70 C71 C72			CE04W1A101M C91-0117-05 CS15E1C2R2M CS15E1VR68M CS15E1V010M	CERAMIC TANTAL TANTAL	100UF 10WV 0.01UF K 2.2UF 16WV 0.68UF 35WV 1.0UF 35WV	
C73 C74 C76 C77 C79 ,80			CEO4W1A470M C91-0117-05 CK45B1H102K CEO4W1HR47M C91-0117-05	CERAMIC CERAMIC ELECTRO	47UF 10WV 0.01UF K 1000PF K 0.47UF 50WV 0.01UF K	
C81 C82 -85 C86 C87 C88			C092M1H333K CEO4W1H01OM CK45B1H1O2K CEO4W1C47OM CEO4W1H01OM	ELECTR® CERAMIC ELECTR®	0.033UF K 1.0UF 50WV 1000PF K 47UF 16WV 1.0UF 50WV	
C89 C90 C93 C94 C95			CK45B1H152K CEO4W1C100M CEO4W1A470M CK45B1H102K CEO4W1H0R1M	ELECTR® ELECTR® CERAMIC	1500PF K 10UF 16WV 47UF 10WV 1000PF K 0.1UF 50WV	
C96 C97 C98 C99 C100			CC45SL1H101J CK45B1H102K C91-1008-05 CQ92M1H153K CQ92M1H183K	CERAMIC CERAMIC MYLAR	100PF J 1000PF K 0.022UF K 0.015UF K 0.018UF K	
C101 C102 C103 C105 C106			C092M1H333K CE04W1A470M C092M1H473K CE04W1A470M C91-0117-05	ELECTR® MYLAR ELECTR®	0.033UF K 47UF 10WV 0.047UF K 47UF 10WV 0.01UF K	T1,W1 T1,W1
C107 C108 C109,110 C115			C91-0433-05 C092M1H333K C092M1H103K CK45B1H102K	MYLAR MYLAR	0.0039UF K 0.033UF K 0.010UF K 1000PF K	T1,W1 T1,W1 T1,W1
L1 ,2 L3 L4 L4 L5			L31-0267-05 L15-0306-05 L79-0498-15 L79-0499-05 L34-0683-05	COIL LOW-FREQUENCY HELICAL RESON HELICAL RESON COIL	IATOR	T1,W1 K1,K2,M1-M4
L6 L7 L8 L9 L10 ,11			L30-0281-15 L30-0005-05 L71-0216-05 L30-0005-05 L40-1021-12	IFT IFT MCF IFT SMALL FIXED I	(10.24MHZ) (10.695MHZ) (10.695MHZ) (NDUCTOR (1UH)	
L12 L13 L14 L15 L16		*	L79-0446-05 L40-1001-14 L77-0858-15 L30-0531-05 L72-0315-05		INDUCTØR (10UH) HATØR (10.24MHZ) (455KHZ)	

* New Parts

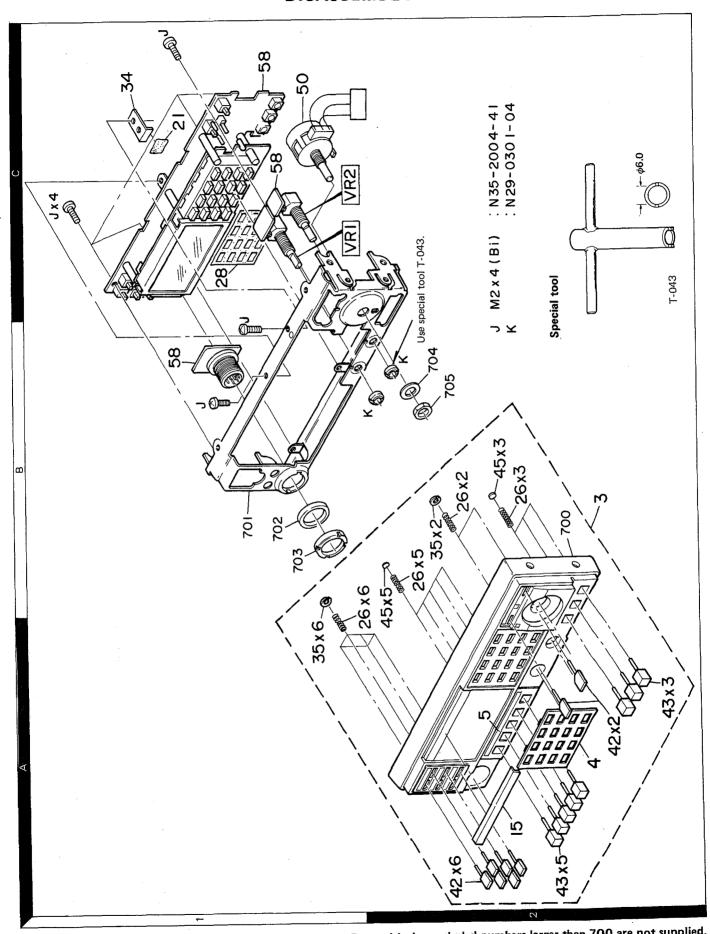
PARTS LIST

Parts without Parts No. are not supplied.

Les articles non mentionnes dans le Parts No. ne sont pas fournis.

Ref. No.	Address New Parts No.		Parts No.	Description	Desti-	
参照番号	1 44	Parts 新	部品番号	部品名/規格	nation 仕 向	
L17			L30-0503-05	IFT (455KHZ)		
R42 R66 VR1 VR2 -4 VR5		*	RS14KB3D33OJ RN14BK2B91O2F R12-2413-O5 R12-4413-O5 R12-3443-O5	FL-PR00F RS 33 J 2W RN 91.OK F 1/8W TRIMMING P0T. (5K) TRIMMING P0T. (5OK) TRIMMING P0T. (1OK)	T1,W1 K1,K2,M1—M4	
VR6 VR7 VR8			R12-4413-05 R12-2413-05 R12-3521-05	TRIMMING POT. (50K) TRIMMING POT. (5K) TRIMMING POT. (20K)	T1,W1	
D1 ,2 D1 ,2 D3 ,4 D5 ,6 D5 ,6			1N4448 1S1555 1N60 1N4448 1S1555	DIBDE DIBDE DIBDE DIBDE		
D7 D8 D8 D9 D10			MTZ11JC 1N4448 1S1555 MTZ6.2JA 1N60A	ZENER DIØDE DIØDE DIØDE ZENER DIØDE DIØDE	T1,W1	
IC1 IC2 IC3 IC4 IC5		*	UPC1241H TA7761P MB3756 NJM78MO6A NE555P	IC IC IC IC IC	T1,W1	
Q1 Q2 Q3 ,4 Q5 Q6 ,7			35K129(S) 35K74(L) 25C2668(Y) 25C496(Y) 25C2458(Y)	FET FET TRANSISTØR TRANSISTØR TRANSISTØR		
08 09 010 •11 TH1			2SC1775(E) 2SC2458(Y) DTC114ES 112-502-2	TRANSISTØR TRANSISTØR DIGITAL TRANSISTØR THERMISTER (SK ØHM)		
		***	X59-1000-10 X59-1010-10 X59-1020-10 X59-1030-10 X59-1040-10	MIC AMP MODULE UNIT MIC AMP,S-METER UNIT ALERT,VACANT-CH UNIT CENTER DETECTOR UNIT SQUELCH CONTROL UNIT		

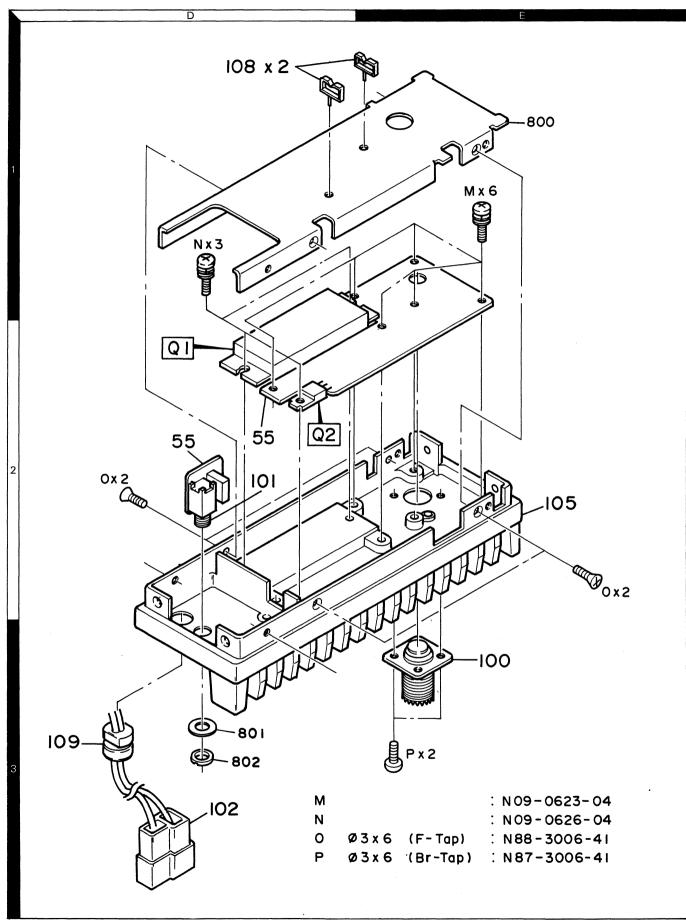
DISASSEMBLY



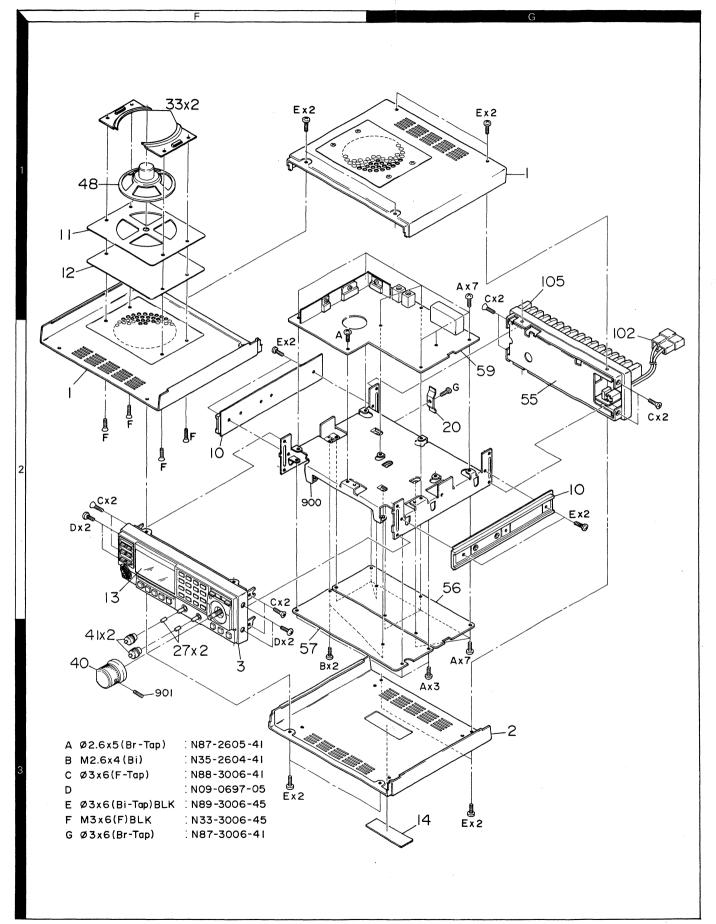
Parts with the exploded numbers larger than 700 are not supplied.

DISASSEMBLY

DISASSEMBLY



Parts with the exploded numbers larger than 700 are not supplied.



Parts with the exploded numbers larger than 700 are not supplied.

ADJUSTMENT

REQUIRED TEST EQUIPMENT

1. DC V.M

1) High input impedance

2. RF VTVM (RF V.M)

1) Input impedance : 1M Ω min., 2pF max.

2) Voltage range : F.S = 10mV ~ 300V

3) Frequency range: Up to 150MHz

3. Frequency Counter (f. counter)

1) Input sensitivity: Approx. 50mV

2) Frequency range: Up to 150MHz

4. DC Power Supply

1) Voltage: 10V ~ 17V, variable

2) Current: 8A min.

5. RF Power Meter

1) Measurement range Approx.: 50W

2) Input impedance : 50Ω

3) Frequency range: 144MHz

6. AF VTVM (AF V.M)

1) Input impedance : $1M\Omega$ min.

2) Voltage range: F.S = $1 \text{mV} \sim 30 \text{V}$

3) Frequency range: 50Hz ~ 10kHz

7. AF Generator (AG)

1) Output frquency: 100Hz ~ 10kHz

2) Output voltage: 0.5mV ~ 1V

8. Linear Detector

1) Frequency range: 144MHz

9. Field Strength Meter

1) Frequency range: 144MHz

10. Directional Coupler

11. Oscilloscope

1) High sensitivity oscilloscope with horizontal input terminal

12. SSG

1) Frequency range: 144~149MHz

2) Modulation: AM and FM MOD.

3) Output level: -20dB to 100dB

13. Dummy Load

1) 8Ω , 5W (approx.)

14. Noise Generator

1) Must generate ignition-like noise containing harmonics beyond 150MHz.

15. Sweep Generator

1) Sweep range: 144~149MHz

16. Tracking generator

PREPARATION

1) Unless otherwise specified, knobs and switches should be set as follows **Table 10.**

POWER SW	ON	SQUELCH VR	MIN
AL. SW	OFF	AF GAIN VR	MIN
PRIO SW	OFF	PHONE SW	OFF
LAMP SW	OFF	TONE SW	OFF
REV SW	OFF	VOICE SW	OFF
SCAN SW	OFF	DCL SW	OFF
LOW SW	OFF	C. SQ SW	OFF
F. LOCK SW	OFF	CS SW	OFF

Table 10

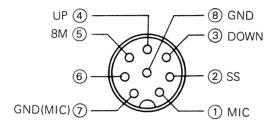


Fig. 20 MIC terminals (view from front panel side)

- Use an insulated adjusting rod to adjust trimmers and coils.
- 3) To prevent damaging SSG, never set the stand by switch to SEND while adjusting the receiver section.
- 4) Be sure to turn the power switch OFF, before connecting the power cable to a power source.
- 5) SSG output levels are those at the time the output terminal is open.

ADJUSTMENT

RX ADJUSTMENT

NOTE: COMP = COMPOUND UNIT (X60-1250-10.-51)

		Me	asureme	nt		А	djustment	
Item	Condition	Test equipment	Unit	Terminal	Unit	Part	Method	Specification/Remarks
1. Reset	Set the power SW on, while depressing PS key. Then, release the PS key. When the lithium battery is	Display						Tone sounds.
	replaced, the microprocessor must be reset. Set the power SW on, while depressing PS key and terminal shorted (near Q6 position) on CONTROL unit by driver, etc. Then, release the PS key.						Terminal	
2. Voltage	1) Power supply: 13.8V DC	DC VM	COMP.	8R		JP18	+4	7.8V-8.6V
check	SQL VR MIN (fully CCW)			8C		7-1		7.8V-8.6V
				6C		10-1		5.6V-6.4V
				8T		11)-4		0.3V or less
	2) Transmit.			8T				7.8V-8.6V
				8R		JP18		0.3V or less
			PLL	DB				7V-13.5V
3. PLL	1) IF level Display: 6.000 (K,M) 5.000 (W,T)	SCOPE (10:1 probe)	PLL	TP1 (R74)	PLL	L14, L15	MAX When PLL leep is unlocked, ADJ TC4.	2.0Vp-p or more.
	2) RX VCO (RX) Display : 2.000 (K,M) 4.000 (W,T)	DC V.M		TP2 (C55)		TC4	6.0∨ (K,M) 5.0∨ (W,T)	±0.1V
	3) TX VCO Display: 2.000 (K,M) 4.000 (W,T)			TP3 (R17) (collector Q5)		TC1	2.5V (K,M) 3.0V (W,T) When PLL loop is un- locked, ADJ TC1.	(1.5V—2.9V) (K,M)
-	4) Receive Display: 7.995 (K,M) 5.995 (W,T)			TP2 (C55)				4.0V—6.0V 3.5V—4.5V Confirm.
	5) Transmit Display: 7.995 (K,M) 5.995 (W,T)			TP3 (R17)				4.5V–5.5V Confirm.
	6) HET, Frequency Receive Display: 6.000 (K,M) 5.000 (W,T)	f.counter		LR (② -6)		L17	135.305MHz ADJ. 134.305MHz ADJ.	±100Hz ±100Hz
4. Helical	1) Connect the sweep gen. to ANT terminal. Output: 10dB Disconnect the No. 3 connector (SF, GND).	Detctor SCOPE	COMP.		COMP.	L2, L4	Adjust to obtain the waveform as right fig.	144 148 (K,M) 144 146 (T,W)
;				IN (TP1)	25本		OUT (Oscilloscope)	
5. SF level		DE V/M	COMP	CE.	COMP.	16	MAX	0.2V or more
6. GAIN	1) Display: 6.060 (K,M) 5.060 (T,M) ANT: SSG Output: 5dBµ MOD.: 1kHz DEV. 3kHz f: 146.06MHz (K,M) 145.06MHz (W,T)	DC V.M (3.0V range)	COMP.	-	COMP.	L5, L7, L9, L15	MAX Minimum voltage reading, Repeat 2 or 3 times.	Ref. 2.35V

ADJUSTMENT

		Me	Measurement			А	djustment	
ltem	Condition	Test equipment	Unit	Terminal	Unit	Part	Method	Specification/Remarks
7. DISCRI S/N	1) ANT 6.060 (K,M) 5.060 (W,T) ANT SSG MOD.: 1kHz, DEV. 3kHz Output: 60dBµ	AF V.M	Rear panel	SP	COMP.	L17	MAX	
	2) ANT SSG Output: -6dBµ (144.00-147.98MHz)							12dB SINAD or more.
8. S-Meter	1) MOD. 1kHz: DEV. 3kHz 6.060 (K,M) 5.060 (W,T) SSG output: 16dBµ	1	Front panel		COMP.	VR6	S-LCD should light.	
9. Vacant CH level	1) SSG output : -8dBµ MOD/DEV. OFF Short both TP3 terminals.				COMP.	VR3	Adjust threshold point.	
10. SQ SEN.	1) Threshold point : no signal	1	Front panel	SQL VR			Audio noise will disappear. C.TUNE indicator off.	8:30 – 12:00
	2) SSG output : —12dBµ						C.TUNE indicator lights.	SQ open.

TY ADJUSTMENT

	Condition	Measurement			Adjustment			
Item		Test equipment	Unit	Terminal	Unit	Part	Method	Specification/Remarks
1. DRIVE	1) Transmit. Display: 6.000 (K,M) 5.000 (W,T) Disconnect the coax. cable from the DO terminal in the COMP. unit. Connect the power meter to the DO terminal in the COMP. unit.	POWER (3W) DO termi		E31-2167	0102-05	TC2,3	MAX In case of TM-2550A/E, reduce RF output from peak point to 0.04W down by TC3 (capacity of TC3 will decrease the position.)	0.5–0.7W 0.25–0.45W (TM-2530A)
2. POWER	1) Connect the coax, cable from the DO terminal in the COMP. unit. Display: 7.995 (K,M) 5.995 (W,T)	1	Rear panel	ANT	FINAL	VR3	47W ADJ. (K,M) 28W ADJ. (W,T) RF LCD	47W±0.5W 28W±0.5W All RF-LED's should light.
	2) 4.000-7.995 (K,M) 4.000-5.995 (W,T)							43–54W or less. 9.5A or less (K,M) 24–33W or less. 6.5A or less (W,T)
3. LOW POWER	1) HI/LOW SW: LOW Display: 6.000 (K,M) 5.000 (W,T)	Power meter		ANT	FINAL	VR4	5W ADJ.	5W±0.5W
4. RF METER	1) Confirm, then, HI/LOW SW: HI	RF-LCD			COMP.	VR4	Adjust 5 LED's reading.	
5—1 PROTEC- TION (null)	1) FINAL unit 2) Display : 6.000 (K,M) 5.000 (W,T)				FINAL	VR2	MAX	
	Transmit.	Power meter	Rear panel	ANT				45W or more (K,M) 25W or less (W,T)
		DC multi- meter	FINAL	TP	FINAL	VR1	MIN	0.5V or less.

ADJUSTMENT

ltem	Condition	Measurement			Adjustment			
		Test equipment	Unit	Terminal	Unit	Part	Method	Specification/Remarks
5—2. PROTEC- TION	1) ANT 45/25W : Open 45/25W short	DC supply V.M			FINAL	VR2	4A/3A ADJ.	6A or less (K,M) 5A or less (W,T)
(Current)	After adjusted, connect the power meter.					<u></u>		
6. MIC GAIN DEV.	1) Turn VR7 fully CCW. MIC 50mV/1kHz (K,M) 30mV/1kHz (T,W)	Linear detector			COMP.	VR7 VR5	±4.5kHz ADJ.	±100Hz—
	2) MIC 5mV/1kHz (K,M) 3mV/1kHz (T,W)					VR7	±3.0kHz ADJ.	±100Hz
7. FREQ. check	1) Display : 6.000 (K,M) 5.000 (W,T)	f.counter						146.000±500Hz (K,M) 145.000±500Hz (W,T)
8. TONE (K,M)	1) Install TU7 in the CONTROL Press TONE key then, turn the encoder dial to 88.5kHz transmit.	Linear detector f.counter						DEV. 0.6-0.9kHz FREQ. 88.5kHz Check output wave of linear detector output. display.
9. TONE (W)	1) Press TONE key. (Transmit after ADJ.)				COMP.	VR8	1.750Hz	±10Hz DEV. : 2.5kHz or more
10. TONE Burst (T)	Remove the TH/E connector, Connect 5V DC power supply to the TH/E connector (Transmit)				СОМР.	VR8	1.750Hz	±10Hz DEV.: 2,5kHz or more
	Connect the TH/E connector. (Transmit) TONE SW: ON	Monitor radio						
11. Touch tone (K,M)	1) Transmit Press '3', '6' key at the same time.	Linear detector f.counter			COMP.	VR1	3kHz ADJ.	±0.2kHz 1471.9Hz±1%
12. SCAN	1) SCAN SW : T0 (Confirm VR1 at 12 o'clock.) SQL VR : MIN (CCW) Key board : Press ''SC''				CONT.	VR1	Check scan time changed when VR1 turn.	
	2) After ADJ. Key board : Press "C"	-			CONT.	VR1	Centered.	

Microprocessor operation check

Item	Condition	Operation check
1. Keyboard	1) SQL : MIN (CCW)	
	Power SW : ON	
	Reset microprocessor.	S
	(See ADJ of "RESET".)	4.000 1
	2) KEY: 1.9. PS. LO (K,M)	Display does not change.
	1. 2. 3. 6. 7. 8. 9. 0.	<u>ছ</u> া
	PS. L0 (T,W)	4.000 1
	3) KEY : 2. 2. 2. 2.	Tone sounds.
	4. 2. 2. 2.	S
		2.220 1
		<u>s</u>
		4.220 1

item	Condition	Operation check
1. Keyboard	4) KEY: 3.3.3.3. 4.3.3.3.	§ 3.330 1
		§ 4.220 1
	5) KEY : 4. 4. 4. 4.	§ 4.440 1
	6) KEY: 5.5.5.5.	5.555 1
	·	

FINAL UNIT (X45-1440-XX) Component side view -10 : TM-2550A (K1,M1,M3), TM-2550E (T,W) -11 : TM-2530A (K2,M2,M4)

Q1: M57726 (TM-2550A/E) M57737(TM-2530A)

2SC2458

Q1: M57726 (TM-2550A/E), M57737 (TM-2530A) Q2: 2SD1406(Y) Q3: 2SA1015(Y) Q4-7: 2SC2458(Y) D1: U15B D2: UM9401 (TM-2550A/E), MI407 (TM-2530A) D3: MI308 D4,5: 1S1587

2SA790

	C1
TM-2550A/E	X
TM-2530A	0

3369-03

DTC124EF

GND

O: Used, X: Not used

TERMINAL FUNCTION

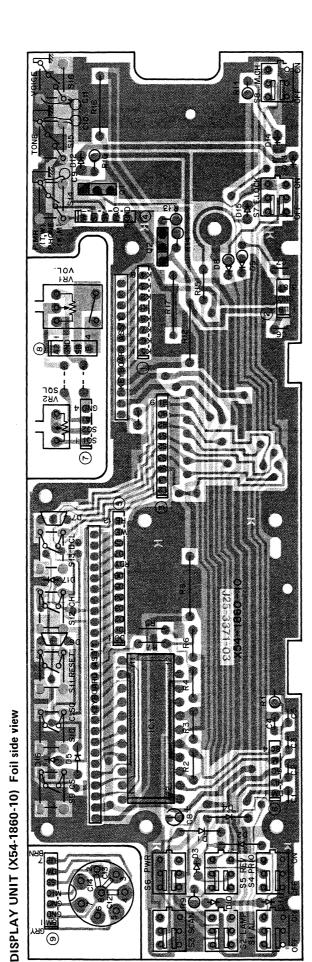
2SD1406

2SA1015

Terminal name	Terminal function
AL	ALERT
AP	Audio Power Output
. A1	AF Output
A2	AF Input
В	+ 13.8V
BD	Busy Display
BZ	Buzzer
CB	Common + B
CT	Tone Clock
CP	PLL Clock
5C .	+ 5V Common
6C	+ 6V Common
8C	+ 8V Common
DB	Drive + B
DT	Tone Data
DW	MIC Down SW
DP	PLL Data
DM	Dimmer
DO	Drive Out
ET	Tone Enable
EP	PLL Enable
E1	Encoder 1
E2	Encoder 2
HL	HI/LOW
G	GND
LB	Lamp + B
LR	RX Local

Terminal name	Terminal function
ME	Modem Enable
MD	Modem Data
MC	Modem Clock
M	Meter
MIC	MIC
MO	Modulator Out
8M	MIC 8V
NC	Non Connection
RD	RX Data
RA	RX Antenna
RM	RF Meter
SF	Standard Frequency
ST	Stand By
SS	Stand By SW
SQS	Squelch Select
SQ1	Squelch 1
SQ2	Squelch 2
SP	Speaker
SB	Switched + B
TO	Tone
TS	Tone SW
TH	Tone Hi
TD	TX Data
TT	Touch Tone Signal
8T	TX + 8V
8C	+ 8V Common
UP	MIC UP SW
VS	Voice SW

PC BOARD VIEWS/TERMINAL FUNCTION TM-2550A/E,2530A



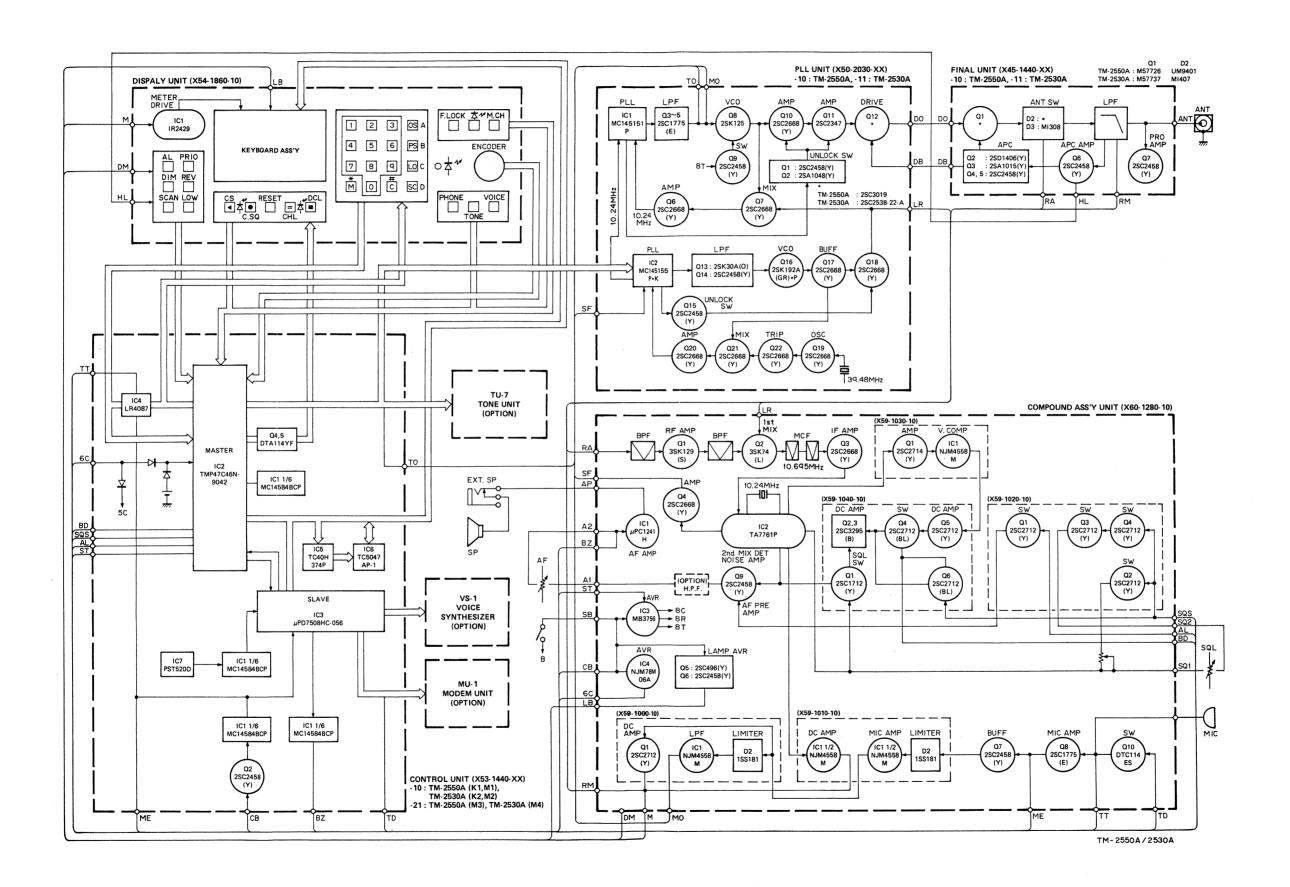
D14: LN222RP D10-13: LN38GPL D6,7: MC921 10 10 10 10

D16,17: LN442YP

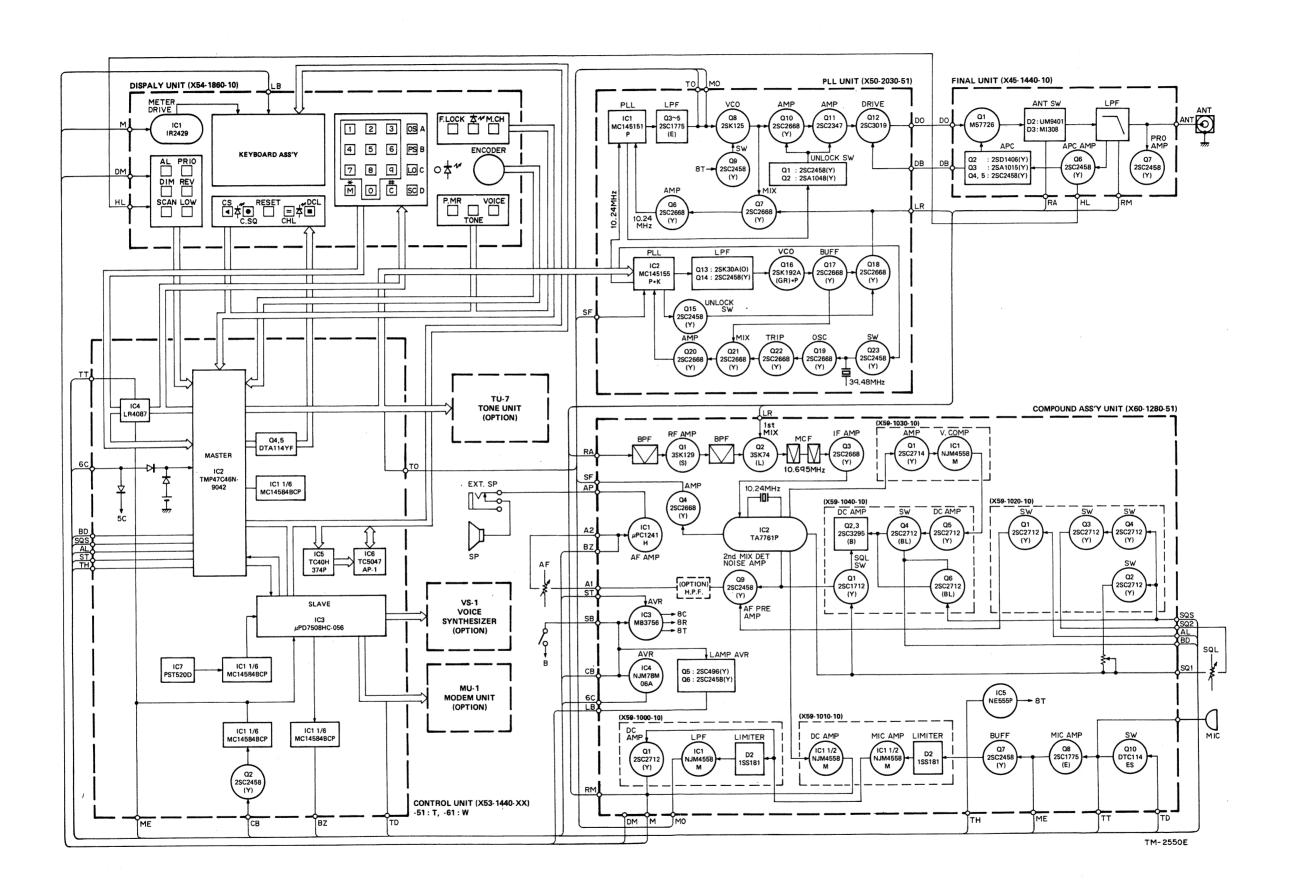
D15: LN322GP

(X54-1860-10)

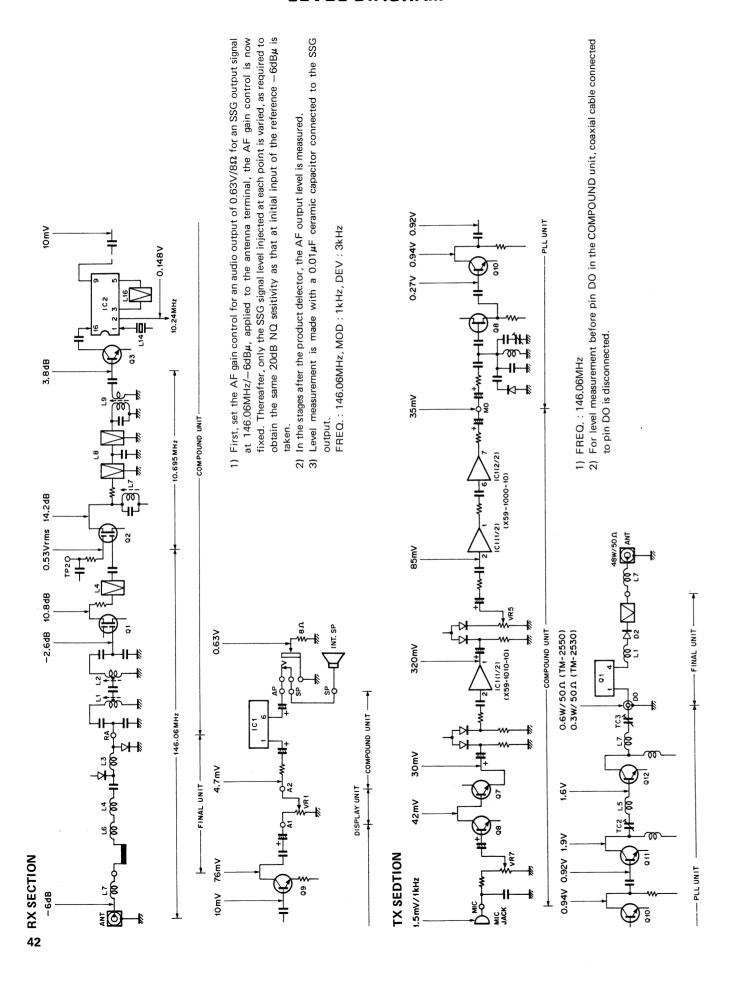
BLOCK DIAGRAM (K,M TYPE)



BLOCK DIAGRAM (T,W TYPE)

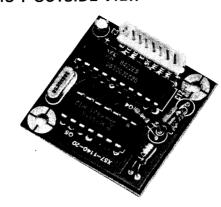


LEVEL DIAGRAM

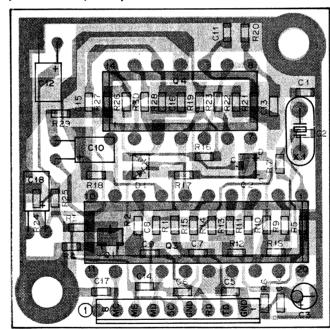


MU-1 (MODEM)

MU-1 OUTSIDE VIEW



MU-1 PD BOARD VIEW (X57-1140-20) Component side view



2SC2712

2SK208





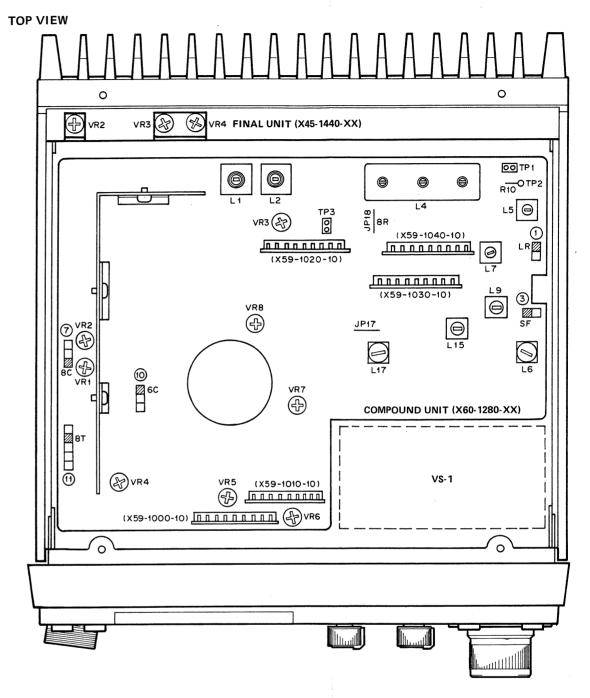
MU-1 PARTS LIST

N	MU-1 (GENERAL) Instruction manual	1	
	Instruction manual	1	
	Cushion	1 1	
N	Carton (Inside) Protective bag	1 2	
	Hex. head boss	1	
	Binding screw	2	
N	MODEM unit	1	
M	ODEM UNIT (X57-1140-20)		
	Chip cap. 15P	2	C1,2
	Floater 22 6.21/	1	C18
	•	1 ' 1	C3,12
		1	C10
	2,000 im 50 v		
	Chip cap. 0.047 µ	1	C13
		1	C17
		1	C8
		7	C4,7,9,11,
			14-16
	Chip cap. 470P	2	C5,6
	Mini-connector 8P	1	
N	X'tal oscillator 3.6864MHz	1	X1
	Chip res. 100Ω	1	R17
	Chip res. $1k\Omega$	1	R1
	Chip res. $10k\Omega$	7	R8,10,12,14,
	Chin 1MO	١,	18,23,30 R16
			R7,9,11,13,15
			R4
		1 '	R26
	-	1	R5,6
	1		R2
		1 '	R3,20,24,25
		1 .	R21,22,27-29
	•	1	R19
	Chip res. 68kΩ	'	l H 19
	Chin TR	1	Q1
		1 '	02
		1	Q3
	l .	1 .	Q4
	10	'	
	Chip diode	1	D1
	M	N MODEM UNIT (X57-1140-20)	Binding screw 2 2 N MODEM UNIT (X57-1140-20) 1

ADJUSTMENT

Item	Condition	Operation check	Item	Condition	Operation check	
1. Keyboard	7) KEY : 6. 6. 6. 6.	⊟ 6.665 1	5. PS KEY	1)Turn the M.CH dial to b CH.	⊕ 5.000 b	
	5. 6. 6. 6.	5.005 I		KEY PS : ON	Tone sounds,	
		5.665 1		2) AL SW : OFF	<u>S</u>	
	8) KEY : 7. 7. 7. 7.			PRIO SW : ON	4.000 b	
	5. 7. 7. 7.	7.775 1 °		W 2210 0W 055	PRIO LCD : Lights on	
		5.775 1	6. SCAN KEY	1) PRIO SW : OFF KEY SC : ON	PRIO LCD: Lights off	
	9) KEY : 8. 8. 8. 8. 5. 8. 8. 8.	§ 8.885 1			4.005 b Tone sounds.	
	3. 0. 0. 0.	S 5.885 1			The dispaly should step up in 5kHz approx. every 6	
	10) KEY: 8.9.9.9. 5.9.9.9.	8.995 1	7. Lamp SW	1) LAMP SW : ON	seconds. Key board illuminated.	
		5.995 1		2) OFF after checked. KEY C : ON		
	11) KEY : 5. 0. 0. 0.	± 5.000 1	8. DCL function	1) Install MU-1 in the CONT. unit.	DCL LED : Lights on.	
	12) KEY : OS	Tone sounds. ⊞	(Digital channel	2) Reset microprocessor. See ADJ of "RESET".		
		5.000 1	link)	3) DCL KEY : Press		
	13) Turn the M.CH dial to CCW 1 step.	5.000 2		4) Memory write to channel "A".		
	14) KEY : M	Tone sounds.		5) Digital code setting		
	15) Turn the M.CH dial to CCW.	LCD Memory CH number should indicate.		CS KEY : Press	00000	
	2. 3. 4 19. A. b. d.	Tone sounds.		• C.SQ KEY : Press	☆ lights.	
	1. 2.	U→1 (or 2→1)		RESET KEY : Press	1_00	
2. M. CH SW	1) F. LOCK SW: ON	Display does not change		• CS KEY : Press again. KEY : Press 4. 9. 4. 0.	4940	
		when the M.CH dial turn to right and left.		6) Check DCL operation.		
		to right and left.		Set the monitor's radio.		
		5.000 2		Condition:		
	2) M.CH key: Press	Lights on		Display: 4.940DCL KEY: Press	Both radio frequency changed display to "A"	
	3) Turn the M.CH dial to the	Tone sounds.		CHL KEY : Press	channel.	
	right 1 step.	§ 4,000 1			Tone sounds.	
	4) KEY : L0	Tone sounds.	9. DCS	1) C.SQ KEY : Press	C.SQ LED's light.	
	47 KE 1 . E0	S	System	DIAL II AN DET	Squelch closed.	
		4.000 ☆ 1	(Digital code	2) Monitor's radio Mic PTT : Press	C.SQ LED's light off Noise heard from SP.	
3. REV.	1) M.CH KEY : Press	Lights off	squelch)	11033	Troise rieara mem en :	
PRIO SW	2) REV SW : ON	± 5.000 1	10. VS-1	1) Install the VS-1 in the	Speaks during display's	
		5.600 1	check	CONT. unit.	condition.	
		REV LCD: Lights on		VOICE KEY : Press	Confirm.	
	3) REV SW : OFF	S	1	1) PHONE KEY : Press 2) Turn to encoder dial.	Display shows A1—A15.	
	PRIO SW : ON	4.000 ± 1 REV LCD : Lights off	Tunction	3) Set "A1" display		
		PRIO LCD : Lights on		PS KEY : Press		
4. AL SW	1) PRIO SW : OFF SQL VR : MIN (CCW)	5.000 1		4) Input 7 digit for telephone number (ex. 6399000)	Telephone indicator light on.	
	AL SW : ON	PRIO LCD : Lights off		5) PHONE KEY : Press again		
	VOL VR: 10:00	Noise should momentarily		6) PHONE KEY : Press	Dial tone 7 digits are trans-	
		mute approx. 0.3 seconds every 6–7 seconds.		during transmit	mit automatically.	
		5.50 y 5.		7) Return to receive mode. PHONE KEY: Press again		
				8) C KEY : Press	A	
				9) PHONE KEY : Press	Telephone indicator light off.	
					light off.	

ADJUSTMENT



COMPOUND UNIT

L5,7,9,15 : IF GAIN adj.

VR1 : TOUCH TONE adj. (K,M only)

VR2 : BEEP LEVEL adj.
VR3 : VACANT CH., LEVEL adj.

VR4 : RF-METER adj.

VR5 : DEV. adj. ±4.5kHz

VR6 : S-METER adj.

VR7 : MIC GAIN adj. ±3kHz

VR8 : TONE adj. (T,W only)

FINAL UNIT

VR2 : Protection current

VR3 : High power

VR4 : Low power

ADJUSTMENT

00 TP1

R10 OTP2

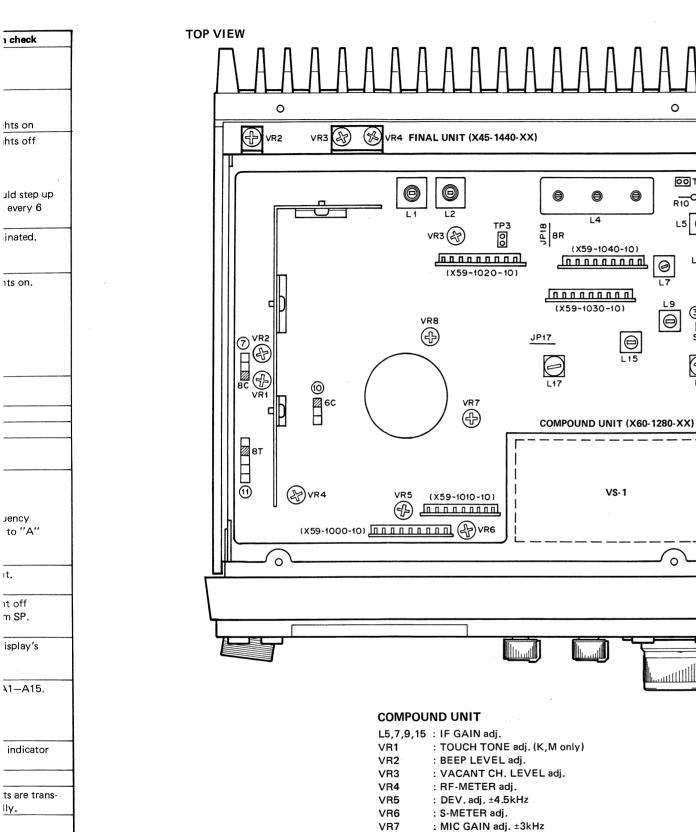
L5 🖨

LR

3 SF

E6

0



ndicator

VR8 : TONE adj. (T,W only)

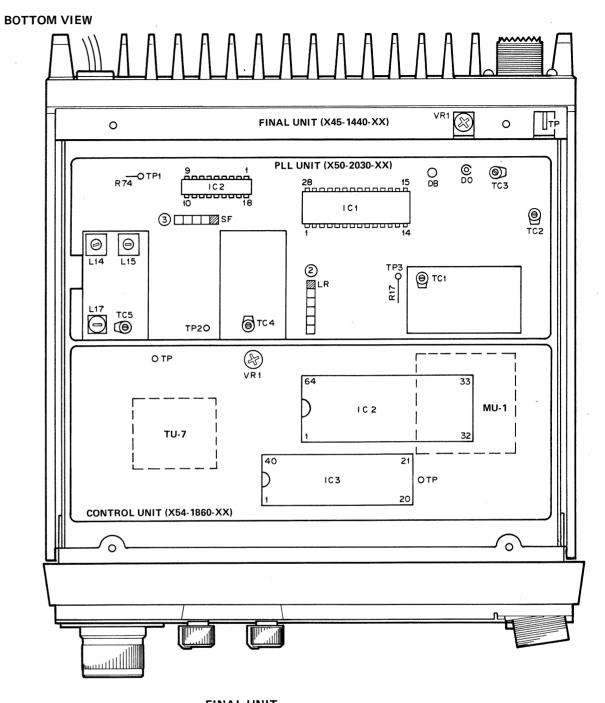
FINAL UNIT

VR2 : Protection current

VR3 : High power

VR4 : Low power

ADJUSTMENT



FINAL UNIT

VR1 : NULL POINT

PLL UNIT

: TX VCO (2.0V) TC4 : RX VCO (6.0V)

L14,15 : PLL IF

: 135.305MHz adj. (K,M type)

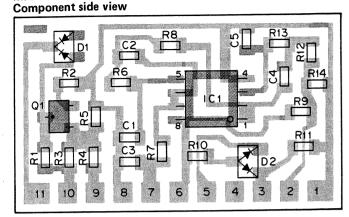
134.305MHz adj. (T,W type)

CONTROL UNIT

: SCAN SPEED

TM-2550A/E,2530A PC BOARD VIEWS

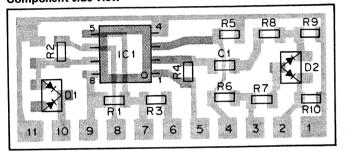
MIC AMP MODULE UNIT (X59-1000-10)



Q1: 2SC2712(Y) IC1: NJM4558M

D1:1SS184 D2:1SS181

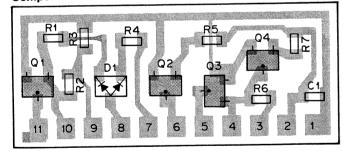
MIC AMP, S-METER UNIT (X59-1010-10) Component side view



IC1: NJM4558M

D1: 1SS184 D2: 1SS181

ALERT, VACANT-CH UNIT (X59-1020-10) Component side view



Q1-4: 2SC2712(Y) D1: 1SS181



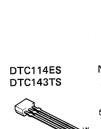




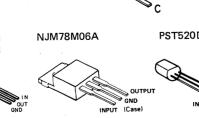


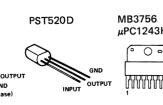


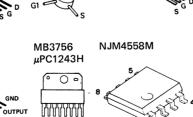




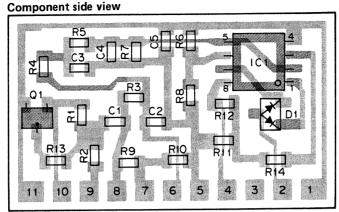








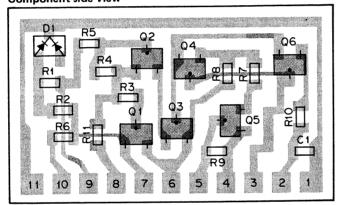
CENTER-DETECTOR UNIT (X59-1030-10)



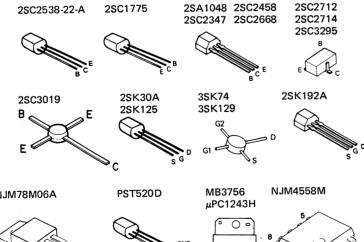
Q1: 2SC2714(Y) IC1: NJM4558M

D1: 1SS181

SQUELCH CONTROL UNIT (X59-1040-10) Component side view

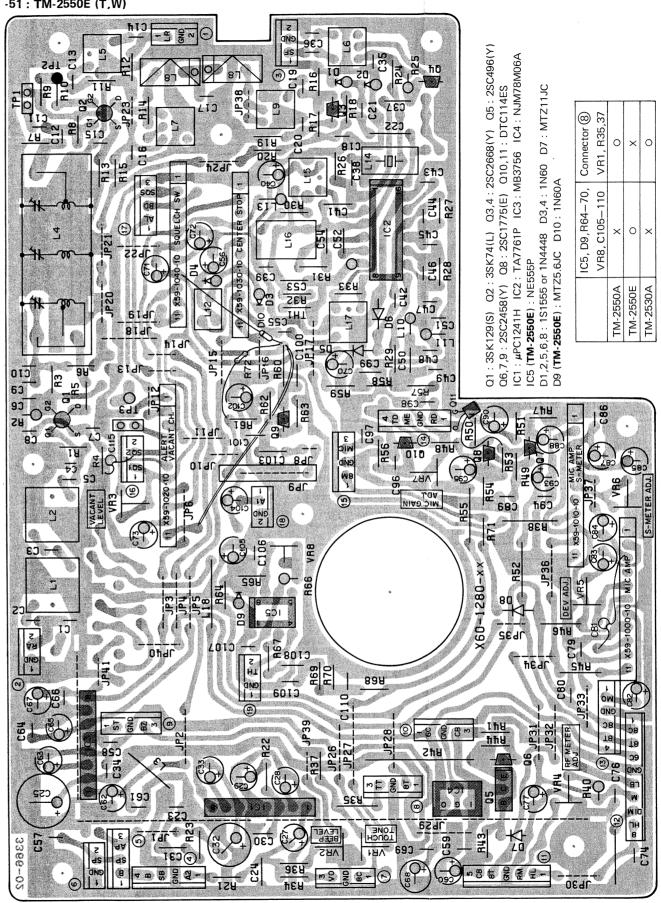


Q1,5: 2SC2712(K) Q2,3: 2SC3295(B) Q4,6: 2SC2712(BL) D1: 1SS184

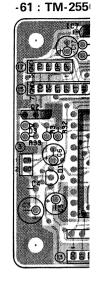


COMPOUND UNIT (X60-1280-XX) Component side view -10 : TM-2550A (K1,M1,M3), TM-2530A (K2,M2,M4)

-51: TM-2550E (T.W)



CONTROL U -10: TM-255 -21 : TM-255 -51 : TM-255



Q1: DTC124EF Q4,5 : DTA114 IC1: MC14584E IC4 (K1, K2, M1. IC6: TC5047AF D1: MC911 D2 D9 (**K1, K2, M1,** D10,12 (T,W): D13-16,22,28 D23 (K1, K2, M1

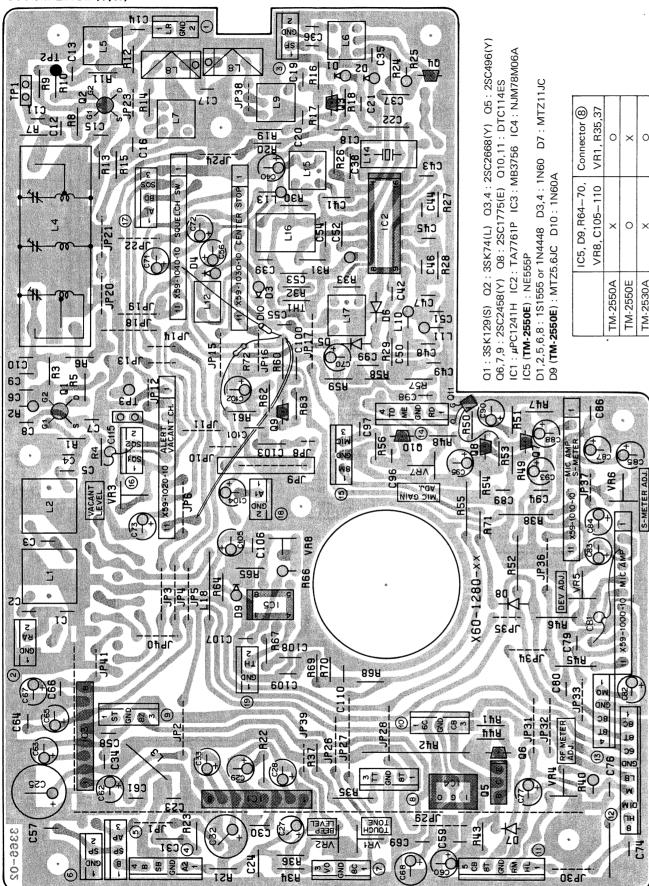
PLL UNIT (X -10 : TM-255 -11 : TM-253 -51: TM-255



Q1,9,14,15:2S Q6,7,10,17-22 Q12 (TM-2550A Q13: 2SK30A((IC1: MC14515 D1,4:1S1555 D7 (TM-2550E)

COMPOUND UNIT (X60-1280-XX) Component side view -10: TM-2550A (K1,M1,M3), TM-2530A (K2,M2,M4)

-51 : TM-2550E (T,W)

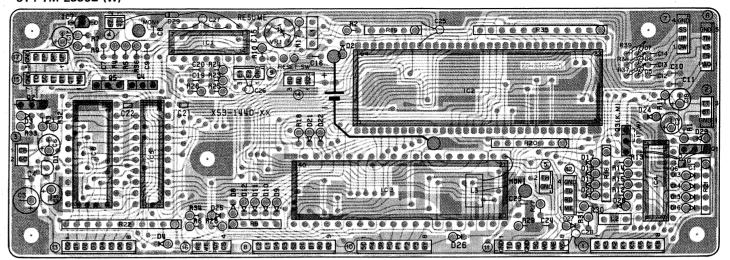


CONTROL UNIT (X53-1440-XX) Component side view

-10: TM-2550A (K1,M1), TM-2530A (K2,M2)

-21: TM-2550A (M3), TM-2530A (M4)

-51: TM-2550E (T) -61 : TM-2550E (W)



Q1: DTC124EF Q2: 2SC2458(Y) Q3 (K1,K2,M1-M4): DTA114YF

Q4,5: DTA114YF Q6 (K1, K2, M1-M4): DTC143TS

IC1: MC14584BCP IC2: TMP47C46N-9042 IC3: μPD7508HC-056

IC4 (K1.K2.M1-M4) : LR4087 IC5 : TC40H374P

IC6: TC5047AP-1 IC7: PST520D

D1: MC911 D2,4-8,11,17-21,24,27: 1S1555 or 1N4448 D9 (**K1,K2,M1,M2,W**): 1S1555 or 1N4448

D10,12 (T,W): 1S1555 or 1N4448

D13-16,22,28 (K1,K2,M1-M4): 1S1555 or 1N4448

D23 (K1, K2, M1-M4): MTZ5.6JC D25, 26: 1SS106 D29: 1SS133

		Q1,3,6, D13-16,22,23,28, IC4, L2,	D9	D10,12
		C1,2,18, R1,11–15, Connector 2		
TM-2550A	K1,M1	0	0	X
	МЗ	0	X	X
TM-2550E	Т	X	X	0
	W	X	0	0
TM-2530A	K2,M2	0	0	X
	M4	0	X	X

O: Used, X: Not used

PLL UNIT (X50-2030-XX) Component side view

-10 : TM-2550A (K1,M1,M3) -11 : TM-2530A (K2,M2,M4)

-51: TM-2550E (T,W)

9 9 C75 P71 PB D6 C102 R1	
× × × × × × × × × × × × × × × × × × ×	R3 01 E E 012
R89 JF6 C83 R78 3 - 8 8 8 8 6 6 7 C76 R72 015	C29 - R38 O C32 P16 2 9 R 3 L4
8 L14 L15 R76 TU R80 TU TU R80 TU TU R80 TU TU R80 TU TU TU TU TU TU TU T	3 R12 R36 C27 Q11 R36 C28 R35 R35 R35 R35 R35
	05
182	R27 E G S G R24 C25
CHO RECUENT CSD CHO RECUENT CSD CHO RECUENT CSD CHO RECUENT CSD	R20 (1) A G (22)
C56 TP2 P TC4 B RIS RIS RIS RIS	(a) (b) (b) (c) (c) (c) (c) (c) (c) (c) (c) (c) (c

Q1,9,14,15: 2SC2458(Y) Q2: 2SA1048(Y) Q3-5: 2SC1775(E) Q6,7,10,17-22: 2SC2668(Y) Q8: 2SK125 Q11: 2SC2347 Q12 (TM-2550A/E): 2SC3019 Q12 (TM-2530A): 2SC2538-22-A Q13: 2SK30A(0) Q16: 2SK192A(GR)*P Q23 (TM-2550E): 2SC2458(Y) IC1: MC145151P IC2: MC145155P*K

D1,4:1S1555 D2,5:1SV50 D3:1S2208 D6:MTZ6.2JA

D7 (TM-2550E) : MA856

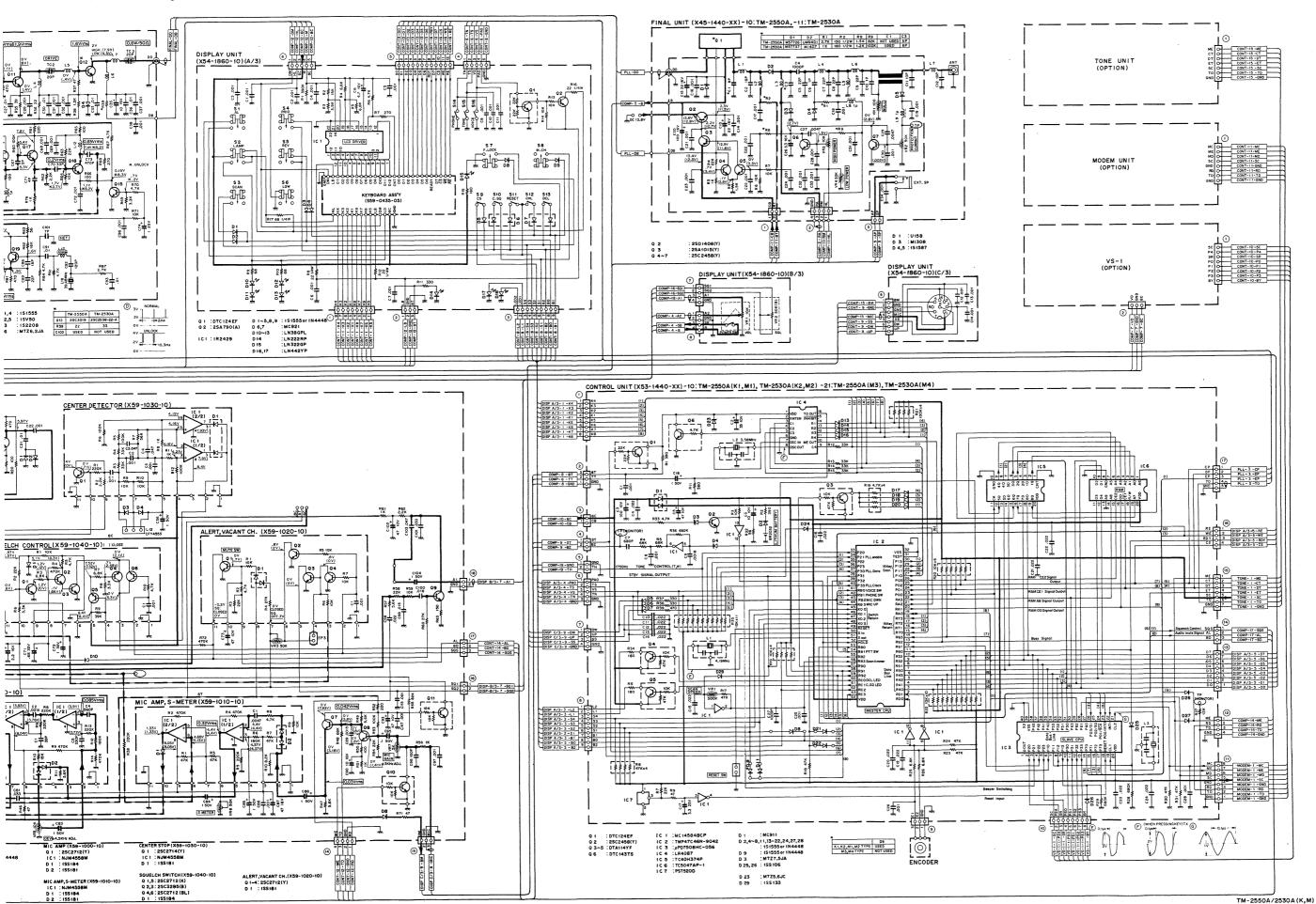
	Q23, D7, TC5, L16, C95, R85,86,88,89	C102
TM-2550A	X	0
TM-2550E	0	0
TM-2530A	X	X

O: Used, X: Not used

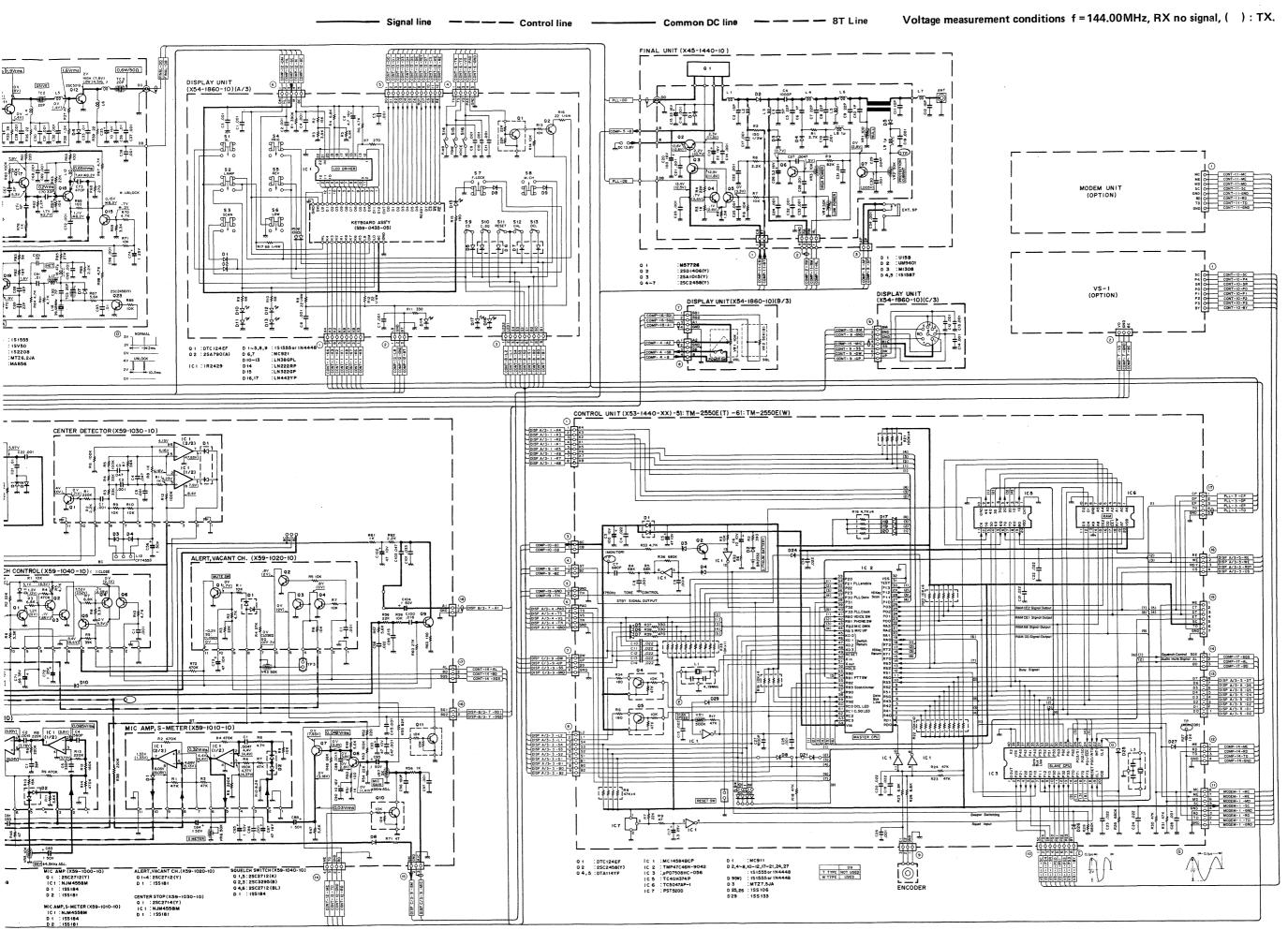
K1,K2, M1,M2,M3,M4





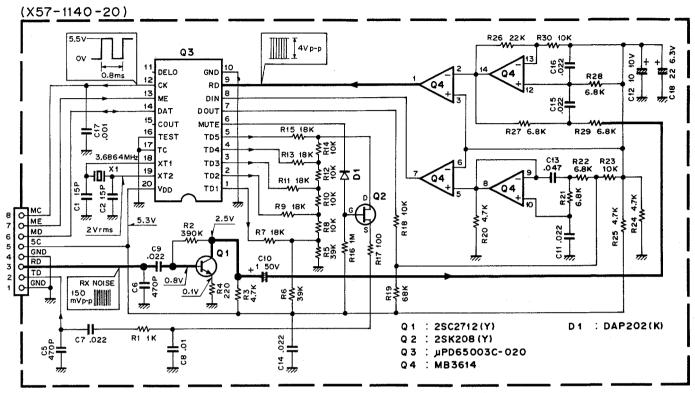


Control line

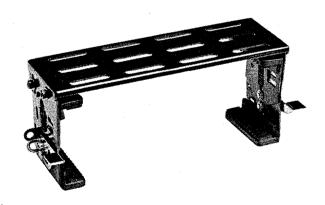


MU-1 (MODEM)/MB-10 (MOBILE MOUNT)

MU-1 SCHEMATIC DIAGRAM



MB-10 OUTSIDE VIEW

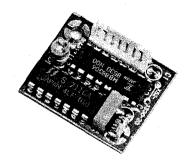


MB-10 PARTS LIST

Part No.	Re- marks	Descrip	otion	ΩТу	Ref. Ņo.
A13-0666-02	N	Mount blacket ass'y		1	
A13-0667-02	N	Mount blacket ass	'y	1	
A13-0668-04	N	Mount hardware		1	
B50-8063-00	N	Instruction manua	il	1	
G13-0823-04		Cushion		4	
H01-8006-03	N	Carton (Inside)		1	
H13-0803-03	N	Protective plate		1	
H13-0805-03	N	Protective plate		1	
H25-0029-04)		Protective bag (Sc	rew etc.)	1	
H25-0036-04		Protective bag (Ar	ngle ass'y)	2	
H25-0116-04)		Protective bag (Ad	ccessary)	1	
N09-0008-04		Hex. head screw	(Accessary)	4	
N09-0632-05		Taptite screw (A)	(Accessary)	4	
N14-0510-04		Flange nut	(Accessary)	4	
N15-1040-45		Flat washer	(Accessary)	4	
N15-1060-46		Flat washer	(Accessary)	4	
N16-0060-46		Spring washer	(Accessary)	4	
N99-0304-04		Hex. hole screw	(Accessary)	4	
W01-0401-05		Hex. wrench	(Accessary)	1	

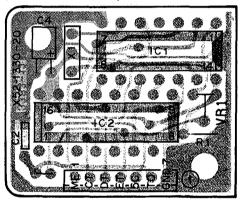
TU-7 (TONE) (K,M TYPE)

TU-7 OUTSIDE VIEW



TU-7 PC BOARD VIEW

(X52-1330-20) Component side view



TU-7 INSTALLATION AND TONE FREQUENCY SETTING PROCEDURE

Available CTSS tone frequencies

		Tee tone medacmone
Hz	Hz	Hz
67.0	114.8	192.8
71.9	118.8	203.5
74.4	123.0	210.7
77.0	127.3	218.1
79.7	131.8	225.7
82.5	136.5	233.6
85.4	141.3	241.8
88.5	146.2	250.3
91.5	151.4	
94.8	156.7	
97.4	162.2	
100.0	167.9	
103.5	173.8	
107.2	179.9	
110.9	186.2	

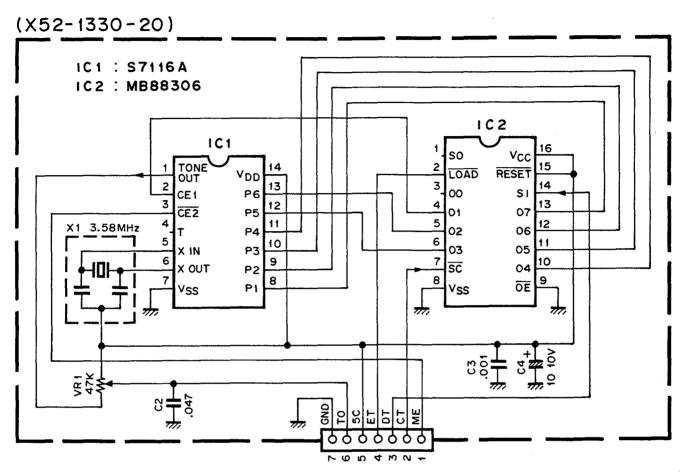
Refer to the instruction manual provided with the transceiver.

TU-7 PARTS LIST

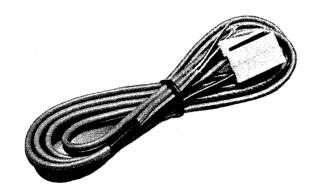
Part No.	Re- marks	Description	αту	Ref. No.		
	TU-7 (GENERAL)					
B50-8045-00	N	Instruction manual	1			
E31-3150-05	N	Cable assembly	1			
G13-0826-04 G31-0826-04	N	Cushion Foam spacer	1			
H01-4679-03 H25-0029-04	N	Carton (Inside) Protective bag	1 2			
J32-0791-04	N	Hex. head boss	1			
N35-2604-41		Binding screw	2			
X52-1330-20	N	Tone unit	1			
T	ONE	UNIT (X52-1330-20)	l			
CE04CW1A100M CK73EB1H473K C91-0757-05		Electro 10μ $10V$ Chip cap. 0.047μ Ceramic 0.001μ	1 1 1	C4 C2 C3		
E40-5021-05		Mini-connector 7P	1			
L78-0018-05	N	Ceramic oscillator	1	X1		
R12-3445-05		Trimming pot. 47k Ω	1	VR1		
MB88306	N	ıc	1	IC2		
S7116A	N	IC	1	IC1		

TU-7 (TONE) (K,M TYPE)/PG-2K (DC POWER CABLE)

TU-7 SCHEMATIC DIAGRAM



PG-2K OUTSIDE VIEW

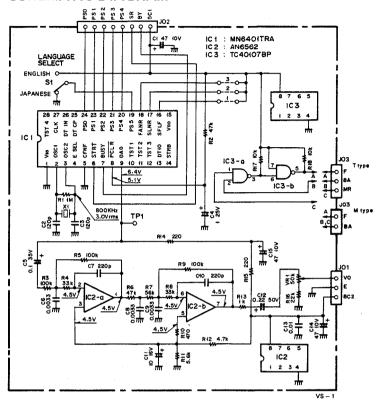


VS-1 (VOICE SYNTHESIZER)

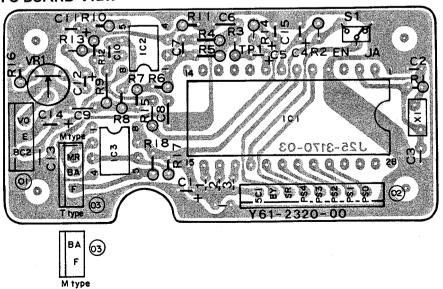
PARTS LIST

PARIS LIST			
Part No.	Re- marks	Description	Ref. No.
B50-4035-00	Ν	Instruction manual	
CC45SL1H121J		C 120P x 2	C2,3
CE04W1A470M CE04W1C100M		E 47 10V E 10 16V	C1,14,15 C11 C12
CE04W1HR22M		E 0.22 50V	CIZ
CK45B1H221K		C 220P × 2	C7,10
CQ92M1H332K		ML 0.0033 x 3	C6,8,9
CS15E1E010M CS15E1V0R1M		T 1 25V T 0.1 35V	C4 C5
C91-0131-05		C 0.01 (SP)	C13
E40-0273-05 E40-0373-05 E40-0373-05 E40-0873-05	Δ Δ Δ	Mini connector 2P M Mini connector 3P M Mini connector x 2 3P T Mini connector 8P	
H01-4481-03 H01-4501-03 H25-0029-04	NΔ	Packing carton (inside) M Packing carton (inside) T Protective bag x 2	
L78-0006-05	N	Ceramic OSC	X1
N89-3006-46		Tapping screw x 4	
R12-4408-05		Trim. pot. 50kΩ	VR1
S31-1411-05	N	Slide switch	S1
AN6562 MN6401TRA TC40107BP	2 2 2	IC IC IC	IC2 IC1 IC3

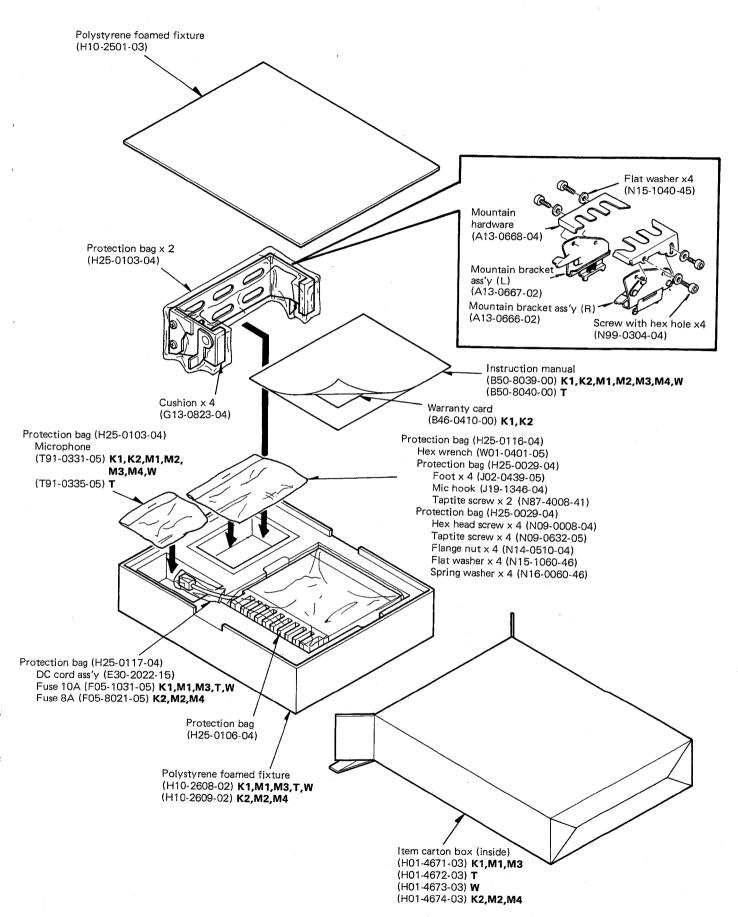
SCHEMATIC DIAGRAM



PC BOARD VIEW



PACKING



TM-2550A/E,2530A SPECIFICATIONS

[General]	
Frequency range	144 MHz to 148 MHz (TM-2550A/2530A)
requestoy rungo	144 MHz to 146 MHz (TM-2550E)
Mode	FM F3 (F3E), F2 (F2D) (Control signal for DCL system)
Antenna impedance	50 ohms
Power requirement	13.8 VDC + 15%
Grounding	Mogative
Grounding	20°C to 150°C / 4°E to 1122°E
Operating temperature	O.S. A in receive mode with no input signal
Current drain	(TM-2550A/2550E)
	Approx, 9.5A in HI transmit mode
	Approx. 3.3 in LOW transmit mode
	, ,
	(TM-2530A)
	Approx. 6.5A in HI transmit mode
	Approx. 2.5A in LOW transmit mode
Dimensions	
	215 mm deep (TM-2550A/2550E)
	195 mm deep (TM-2530A)
	(Projection not included)
Weight	1.95 kg (4.3 lbs) : TM-2550A/2550E
	1.8 kg (4 lbs) : TM-2530A
[Transmitter]	45.M. : (The OFFO A (OFFOF)
Output power (at 13.8 VDC, 50 ohms load)	
	HI: 25 W min. (TM-2530A)
	LOW: 5 W approx.
	(Adjustable up to out 40 W TM-2550A/2550E)
	(Adjustable up to out 20 W TM-2530A)
	Note:
	Recommended duty cycle
	1 minute : Transmission
	3 minutes : Reception
Modulation	Less than 1.15 DDM
Frequency stability Spurious radiation	Less than EO dP
Spurious radiation	Less than -oo db
Maximum frequency deviation Audio distortion (at 60% modulation)	TO KITZ
Audio distortion (at 60% modulation)	Less than 570 (500 Hz to 5000 Hz)
[Receiver]	
Circuitry	Double conversion superheterodyne
Intermediate frequency	1st: 10.695 MHz, 2nd: 455 kHz
Sensitivity	SINAD 12 dB: Less than 0.25 μV
	S+N/N : More than 50 dB at 1 mV input
Selectivity	More than 12 kHz (–6 dB)
	Less than 24 kHz (–60 dB)
Spurious response	Better than 70 dB (except fd-IF/2)
Squelch sensitivity	Less than 0.125 μV (threshold)
Scan stop level	Less than 0.2 μV (threshold)
Output	More than 1.5 W across 8 ohms load (5% distortion)
External speaker impedance	8 ohms
[DCL control]	NP7 agual langth ands
Code	MCV modulation
Modulation	IVION INCOMISATION
Frequency deviation	±3.0 Kmz reference
Mark frequency and deviation	1000 Hz ± 200 FFWI
Space frequency and deviationCode transmission speed and deviation	1200 hts/second ± 200 PPM
Code transmission speed and deviation	1200 bits/3600Hu ±200 FFIVI

Note: Circuit and rating are subject to change without notice due to development in technology.

TRIO-KENWOOD CORPORATION

Shionogi Shibuya Building, 17-5, 2-chome Shibuya, Shibuya-ku, Tokyo 150, Japan

TRIO-KENWOOD COMMUNICATIONS

1111 West Walnut Street, Compton, California 90220, U.S.A.

TRIO-KENWOOD COMMUNICATIONS

DIVISION OF TRIO-KENWOOD ELECTRONICS GmbH

Rembrücker Str. 15, 6056 Heusenstamm, West Germany

TRIO-KENWOOD ELECTRONICS, N.V.

Leuvensesteenweg 504, B-1930 Zaventem Belgium

TRIO-KENWOOD (AUSTRALIA) PTY. LTD. (INCORPORATED IN N.S.W.)

4E. Woodcock Place, Lane Cove, N.S.W. 2066, Australia